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5 Fault location using measurements of current and voltage from  
one end of a line

TECHNICAL FIELD.

The present invention is concerned with a fault location  
technique for a section of a power transmission line utilizing  
10 measurements of current and voltage made at terminals located  
at one end of the section of the power line.

BACKGROUND ART

Several methods and approaches for fault location in high  
15 voltage power transmission systems, and power distribution  
systems, have been developed and employed. One approach has  
been to use voltage/current transducers located at terminals  
located at each of two ends of a section of the power line to  
be monitored. Inductive current transformers are used to  
20 provide a measurement of instantaneous current in a  
transmission line.

US 4,559,491 entitled Method and device for locating a fault  
point on a three-phase transmission line, describes a one-end  
25 fault location (FL) algorithm. High accuracy of fault location  
using a fault locator device at one end of a line is achieved  
by taking into account the actual distribution of a fault  
current in the transmission network. This algorithm has been  
successfully implemented into a product in 1982 and is in  
30 operation with single and parallel transmission lines in many  
countries around the world. However, for certain fault  
conditions it is difficult to obtain accurate pre-fault  
quantities, such as pre-fault currents, in order to calculate  
an estimate for voltage drop across the fault path. Also, a

disadvantage of using phase voltages and currents and zero sequence components of currents is that it is relatively difficult using these values to compensate for shunt capacitance effects. In addition, the fault locator method described is not suitable for single and parallel line sections which have an extra link across the ends of the sections.

#### SUMMARY OF THE INVENTION

The aim of the present invention is to remedy one or more of the above mentioned problems.

This is obtained by a method characterised by claim 1. Specific features of the present invention are characterised by the appending claims.

In one aspect of the invention, a method comprising a new formulation of a one-end fault locator algorithm has been proposed. The uniform description of the transmission network in terms of symmetrical components as well as the generalized models of fault loops and faults have been applied. The resulting advantages include the algorithm can be used for locating faults in typical single and parallel transmission lines, and, in addition, fault location may also be carried out for both single and parallel lines with an extra link between the line ends. Another advantage is that a procedure for calculation of a distance to fault is in the form of a compact quadratic equation with the coefficients dependent on a fault type, acquired measurements and impedance data for the transmission network. Another advantage of the invention is that optimal estimation of the voltage drop across a fault path is applied, which has the result that the pre-fault currents in case of single phase-to-ground faults and phase-to-phase faults are no longer required.

In an embodiment, compensation for shunt capacitances is facilitated by means of the use of the notation of symmetrical components. The distributed long line model of the line has been applied for that. The compensation is performed

5 individually for all the sequences. The currents for particular sequences are compensated for the shunt currents and then the fault loop compensated current is composed. In another embodiment improved accuracy has been obtained by means of an option to measure the source impedance at the  
10 remote end instead of using a representative value. The source impedance measured at the remote end may be considered as sent to the fault locator by using a simple communication means.

In another embodiment, a method for one end fault location for parallel lines to locate single phase-to-ground faults is  
15 described under a plurality of conditions. In another further embodiment a method is described for one end fault location with standard availability of the measured signals for ground faults including both single phase-to-ground faults and phase-to-phase-to-ground faults.

20

In another aspect of the invention, a fault locator device for carrying out the method of the invention is characterised by claim 18. Specific features of the inventive fault locator device are characterised by the appending claims.

25

In another aspect of the invention a computer program is described for carrying out the method according to the invention. In another aspect of the invention a computer  
30 program product comprising a computer program for carrying out the method of the invention is described. In another, further aspect of the invention a graphical user interface is described for displaying a distance to a fault from one end of a section of a power line.

## BRIEF DESCRIPTION OF THE DRAWINGS

A more complete understanding of the method and system of the present invention may be had by reference to the following

5 detailed description when taken in conjunction with the accompanying drawings wherein:

Figure 1 shows in a single schematic diagram a method of fault location in power transmission and/or distribution systems for  
10 parallel lines and single lines according to an embodiment of the invention;

Figure 2a shows a schematic circuit diagram for a parallel transmission network for the positive sequence component in  
15 which the fault loop is marked for the case of a fault locator installed at the terminal AA. Figures 2b, 2c show corresponding diagrams for the negative sequence and zero sequence components, respectively;

20 Figure 3a is a schematic block diagram for obtaining and calculating the phasors of the symmetrical components of voltages and currents used for composing the fault loop voltage. Figure 3b shows a corresponding diagram for composing the fault loop current;

25

Figure 4 shows a circuit diagram for determining the fault current distribution factor for the positive sequence of a single line, in which diagram quantities for the negative sequence are shown indicated in brackets;

30

Figure 5 shows a circuit diagram corresponding to Figure 4 for single lines for determining the fault current distribution factor for the positive sequence of parallel lines, in which quantities for the negative sequence are also shown indicated  
35 in brackets;

Figure 6 shows a schematic diagram for an embodiment of the invention in which source impedance measured at a remote end B may be communicated to a fault locator at the first end A;

5 Figure 7 is a circuit diagram of an embodiment in which the shunt capacitances are taken into account, and shows a positive sequence circuit diagram during a first iteration;

Figure 8 shows a negative sequence circuit diagram for taking  
10 the shunt capacitances effect into account during a first iteration;

Figure 9 shows a zero sequence circuit diagram for taking the shunt capacitances effect into account during a first  
15 iteration;

Figure 10 shows a flowchart for a method for locating a fault in a single line according to an embodiment of the invention;

20 Figure 11 shows a flowchart for a method for locating a fault in parallel lines according to an embodiment of the invention;

Figure 12 and Figures 13a, 13b, 14, 15a and 15b show schematic diagrams of possible fault-types (phase-to-phase, phase-to-ground and so on) with respect to derivation of coefficients for Table 2 in Appendix A2. Figure 12 shows fault types from a-g, and Figures 13a, 13b faults between phases a-b. Figure 14 shows an a-b-g fault. Figures 15a and 15b show symmetrical faults a-b-c and a-b-c-g respectively;

30 Figures 16 and 17 show schematic diagrams for the derivation of the complex coefficients in the fault current distribution factors for the positive (negative) sequence included in Table 3. Figure 16 shows the case of a single line with an extra

link between the substations. Figure 17 shows the case of parallel lines with an extra link between the substations;

Figure 18 shows a fault locator device and system according to  
5 an embodiment of the invention;

Figure 19 shows a flowchart for a method for locating a single phase-to-ground fault in parallel lines in the case of measurements from the healthy line being unavailable,  
10 according to an embodiment of the invention;

Figure 20 shows a schematic diagram for a method of fault location for parallel lines with different modes of the healthy parallel operation;  
15

Figures 21a, shows a schematic equivalent circuit diagram for a parallel network for the incremental positive or the negative sequence. Figure 21b shows an equivalent circuit diagram for the zero sequence while both parallel lines are in  
20 operation. Figure 21c shows the equivalent circuit diagram for the zero sequence with the healthy parallel line switched off and grounded;

Figure 22 shows a flowchart for a method for locating phase-to-phase and phase-to-ground faults in parallel lines in the case of providing the zero sequence currents from the healthy parallel line according to another embodiment of the invention;  
25

Figure 23 shows a schematic diagram fault location for parallel lines with standard availability of measurements according to another embodiment of the invention;  
30

Figures 24 a,b,c show the equivalent circuit diagrams of parallel lines for positive, negative and zero sequence currents respectively.

## 5 DESCRIPTION OF THE PREFERRED EMBODIMENTS

Fig.1 presents a schematic diagram for one-end fault location applied for parallel lines and for a power transmission or distribution system with a single line. A fault locator 1 is positioned at one end 2 of a single line AA-BA 3 or parallel lines AA-BA, AB-BB, 4. A fault F is shown at FA with a corresponding fault resistance, 5, denoted as  $R_F$ . A value for distance to the fault  $d$  from one end 2 determined and provided by the fault locator 1 is indicated with the reference number 7. Components such as parallel line AB-BB and quantities such as a parallel line value zero sequence current  $I_{AB0}$  shown with dotted lines are excluded when considering a single line case.

The fault locator 1 positioned at the first end 2, or 'A' end, is supplied with the following input signals:

- 20 - three-phase voltages ( $V_{AA}$ ) of the faulted line,
- three-phase voltages ( $I_{AA}$ ) of the faulted line,
- zero sequence current ( $I_{AB0}$ ) from the healthy parallel line (zero sequence is not present when the single line only is considered).

25

Fig.2 a,b,c show circuit diagrams of a parallel transmission network for the positive 2a, negative 2b, and zero sequence 2c components. Fault loops for the sequence components 21a, 21b, 21c are shown for the case of a fault locator installed at the terminal AA. An extra link 25 between the terminals A, B is shown. A generalized model of the fault loop considered for different fault types is stated as:

$$V_{AA_P} - dZ_{1LA}I_{AA_P} - R_F(a_{F1}I_{F1} + a_{F2}I_{F2} + a_{F0}I_{F0}) = 0 \quad (1)$$

where:



$d$  - unknown and sought distance to fault,

$Z_{1LA}$  - positive sequence impedance of the faulted line,

$V_{AA,p}$ ,  $I_{AA,p}$  - fault loop voltage and current composed according to the fault type,

5  $R_F$  - fault resistance,

$I_{Fi}$  - sequence components of the total fault current ( $i = 0, i = 1, i = 2$ ),

$a_{Fi}$  - weighting coefficients (TABLE 2).

10 Fault loop voltage and current can be expressed in terms of the symmetrical components of measured voltages/currents:

$$V_{AA,p} = a_1 V_{AA1} + a_2 V_{AA2} + a_0 V_{AA0} \quad (2)$$

$$I_{AA,p} = a_1 I_{AA1} + a_2 I_{AA2} + a_0 \frac{Z_{0LA}}{Z_{1LA}} I_{AA0} + a_{0m} \frac{Z_{0m}}{Z_{1LA}} I_{AB0} \quad (3)$$

where:

15 AA, AB - subscripts used for indicating measurements acquired from the faulted line (AA) and from the healthy line (AB), respectively,

$a_0$ ,  $a_1$ ,  $a_2$  - coefficients which are gathered in TABLE 1 (the Tables are arranged below at the end of the description of

20 embodiments and derivation of these coefficients is shown in APPENDIX A1, also attached).

$Z_{0LA}$ ,  $Z_{0m}$  - impedance of the faulted line and mutual coupling between the lines for the zero sequence, respectively,

$a_{0m} = a_0$  - for parallel lines,

25  $a_{0m} = 0$  - for single lines.

The phasors of symmetrical components of voltages, positive:

$V_{AA1}$ , negative:  $V_{AA2}$  and zero sequence:  $V_{AA0}$  as well as the

phasors of symmetrical components of currents, positive:  $I_{AA1}$ ,

30 negative:  $I_{AA2}$ , zero sequence from the faulted line:  $I_{AA0}$  and

zero sequence from the healthy line:  $I_{AB0}$  are calculated from

the acquired measurements as shown schematically in schematic block diagrams Figures 3a and 3b.

Figure 3a shows an input of instantaneous phase voltages 30a, filtering stage 33a, phasors of phase voltages 31a, calculation of phasors of symmetrical components 33b and phasors of symmetrical components of voltages output at 32a. It may be seen from Fig 3a that acquired phase voltage measurements are subjected to a filter, then calculations are made to find the symmetrical components of the fault loop voltage. Figure 3b shows correspondingly stages used to find the symmetrical components of the fault loop current. Figure 3b shows instantaneous phase currents and instantaneous zero sequence current from the healthy line 30b, filtering 33b, phasors of phase currents and phasor of zero sequence current from the healthy line 31b, calculation 34b and phasors of symmetrical components of currents output at 32b.

Fault loop signals may be composed according to formulae (2)-(3) and TABLE 1, which is the alternative to the classic approach (TABLE 1A, fault loop voltage ( $\underline{V}_{AA\_p}$ ) and current ( $\underline{I}_{AA\_p}$ ), which was used in the fault locator from [1-2].

Voltage drop across a fault path resistance, the third term in (1), can be expressed in terms of the current distribution factors and local measurements of currents which results in:

$$\underline{V}_{AA\_p} - d\underline{Z}_{ILA}\underline{I}_{AA\_p} - R_F \left( \underline{a}_{F1} \frac{\underline{I}_{AA1}}{\underline{k}_{F1}} + \underline{a}_{F2} \frac{\underline{I}_{AA2}}{\underline{k}_{F2}} + \underline{a}_{F0} \frac{\underline{I}_{AA0}}{\underline{k}_{F0}} \right) = 0 \quad (4)$$

Formula (4) has been obtained from the following relations between the symmetrical components of a total fault current and measured currents:

$$\underline{I}_{F1} = \frac{\Delta \underline{I}_{AA1}}{\underline{k}_{F1}}, \quad \underline{I}_{F2} = \frac{\underline{I}_{AA2}}{\underline{k}_{F2}}, \quad \underline{I}_{F0} = \frac{\underline{I}_{AA0}}{\underline{k}_{F0}} \quad (5)$$

where:

$\underline{I}_{F1}$ ;  $\underline{I}_{F2}$ ;  $\underline{I}_{F0}$  - symmetrical components of a total fault current,

$\underline{k}_{F1}$ ,  $\underline{k}_{F2}$ ,  $\underline{k}_{F0}$  - fault current distribution factors for particular sequence quantities,

$\Delta \underline{I}_{AA1} = \underline{I}_{AA1} - \underline{I}_{AA1pre}$ ;  $\underline{I}_{AA2}$ ;  $\underline{I}_{AA0}$  - symmetrical components of currents measured in the line A at the station A (subscript AA); note that in case of the positive sequence the incremental quantity (post-fault current minus pre-fault current) is used.

Voltage drop across the fault path, as shown in the third term in equation (1), is expressed using sequence components of the total fault current. The weighting coefficients  $\underline{a}_{F0}$ ,  $\underline{a}_{F1}$ ,  $\underline{a}_{F2}$ , can accordingly be determined by taking the boundary conditions for particular fault type. See TABLE 2, Alternative sets of the weighting coefficients for determining a voltage drop across the fault path resistance. Examples of derivation of these coefficients are contained in APPENDIX A2.

There is some freedom for setting the weighting coefficients. It is proposed to utilize this freedom firstly for avoiding zero sequence quantities, since the zero sequence impedance of a line may be considered as an unreliable parameter. This can be accomplished by setting  $\underline{a}_{F0} = 0$  as shown in TABLE 2.

Secondly, the freedom in establishing the weighting coefficients can be utilized for determining the preference for using particular quantities. The negative sequence (TABLE 2, set I) or the positive sequence (TABLE 2, set II) can be

preferred as well as possibly both types of the quantities (TABLE 2, set III) can be used for determining the voltage drop across the fault path.

- 5 The set I is recommended for further use, thus avoiding the positive sequence, and thus avoiding the pre-fault positive sequence current, for the largest number of faults. Avoiding the pre-fault positive sequence current is highly desirable since sometimes the pre-fault currents - due to certain
- 10 reasons - can not be recorded or registered, but may be contaminated by one or more the symptoms of the occurring fault. Moreover, the accuracy of recording the pre-fault currents, which are basically lower than the post-fault currents, is not very great. This is so since the A/D
- 15 converters operate with less accuracy in the low range.

Fault current distribution factors depend on the configuration of the transmission network, Figures 4, 5, and impedance parameters. Basically, all impedances for the positive and for

20 the negative sequence are equal to each other and thus one obtains:

$$k_{F1} = k_{F2} = \frac{K_1 d + L_1}{M_1} \quad (6)$$

Coefficients in a fault current distribution factor (6) for a single (Figure 4) and for parallel lines (Figure 5) are

25 gathered in TABLE 3, Coefficients for determining a fault current distribution factor, (note that derivation of the coefficients is shown in APPENDIX A3).

Figure 4 shows a circuit diagram of a single line for

30 determining the fault current distribution factor for the positive sequence currents and with the negative sequence currents such as shown in brackets. Similarly

Figure 5 shows a circuit diagram of parallel lines for determining the fault current distribution factor with

positive sequence currents wherein the negative sequence currents are shown in brackets.

In Fig.4 the extra link 45 between the terminals A, B having

5 impedance for the positive sequence equal to:  $Z_{IAB}$  can be considered as existing ( $Z_{IAB} \neq \infty$ ) or as not present ( $Z_{IAB} \rightarrow \infty$ ).

In Fig.5 the extra link 55 between the terminals A, B having

impedance for the positive sequence equal to:  $Z_{IAB}$  can be

considered as existing ( $Z_{ILB \& AB} = \frac{Z_{ILB} Z_{IAB}}{Z_{ILB} + Z_{IAB}}$ ) or as not present

10 ( $Z_{ILB \& AB} = Z_{ILB}$ ).

Substituting (6) into (4) and adjusting  $a_{F0} = 0$  (as in TABLE 2) results in:

$$V_{AA_p} - d Z_{ILA} I_{AA_p} - \frac{R_F M_1}{K_1 d + L_1} (a_{F1} \Delta I_{AA1} + a_{F2} I_{AA2}) = 0 \quad (7)$$

After multiplying both sides of (7) by:  $\frac{K_1 d + L_1}{I_{AA_p}}$  and some

15 rearrangements, the quadratic formula with two unknowns,  $d$  [p.u.] sought fault distance from A,  $R_F$  - fault resistance, is obtained:

$$K_1 Z_{IL} d^2 + (L_1 Z_{IL} - K_1 Z_{AA_p}) d - L_1 Z_{AA_p} + R_F M_1 \frac{(a_{F1} \Delta I_{AA1} + a_{F2} I_{AA2})}{I_{AA_p}} = 0 \quad (8)$$

where:

20  $Z_{AA_p} = \frac{V_{AA_p}}{I_{AA_p}}$  - calculated fault loop impedance.

Writing formula (8) in more compact form results in:

$$A_2 d^2 + A_1 d + A_0 + A_{00} R_F = 0 \quad (8a)$$

where:

25  $A_2 = A_{2\_Re} + j A_{2\_Im} = K_1 Z_{ILA}$

$$A_1 = A_{1\_Re} + j A_{1\_Im} = L_1 Z_{ILA} - K_1 Z_{AA_p}$$

$$A_0 = A_{0\_Re} + j A_{0\_Im} = -L_1 Z_{AA_p}$$

$$A_{00\_Re} + jA_{00\_Im} = \frac{M_1(a_{F1}\Delta I_{AA1} + a_{F2}I_{AA2})}{I_{AA\_p}}$$

$$Z_{AA\_p} = \frac{V_{AA\_p}}{I_{AA\_p}} - \text{calculated fault loop impedance}$$

$K_1$ ,  $L_1$ ,  $M_1$  - coefficients gathered in TABLE 3.

- 5 Formula (8a) can be written separately for real and imaginary parts:

$$A_{2\_Re}d^2 + A_{1\_Re}d + A_{0\_Re} + A_{00\_Re}R_F = 0 \quad (8b)$$

$$A_{2\_Im}d^2 + A_{1\_Im}d + A_{0\_Im} + A_{00\_Im}R_F = 0 \quad (8c)$$

- 10 Combining (8b) and (8c) in such the way that fault resistance is eliminated [that is, equation (8b) is multiplied by  $A_{00\_Im}$  and equation (8c) by  $A_{00\_Re}$  and then subtracting them] yields the quadratic formula for a sought fault distance:

$$B_2d^2 + B_1d + B_0 = 0 \quad (9)$$

- 15 where:

$$B_2 = A_{2\_Re}A_{00\_Im} - A_{2\_Im}A_{00\_Re}$$

$$B_1 = A_{1\_Re}A_{00\_Im} - A_{1\_Im}A_{00\_Re}$$

$$B_0 = A_{0\_Re}A_{00\_Im} - A_{0\_Im}A_{00\_Re}$$

- 20 Equation (9) has two roots ( $d_1$ ,  $d_2$ ) for the distance to fault:

$$d_1 = \frac{-B_1 - \sqrt{B_1^2 - 4B_2B_0}}{2B_2}$$

$$d_2 = \frac{-B_1 + \sqrt{B_1^2 - 4B_2B_0}}{2B_2}$$

(10)

- 25 The root which fulfils the condition ( $0 \leq d \leq 1$ ) is selected as the solution for the distance to fault.

In another embodiment of the invention, the method of fault location is carried out by using a measurement of source impedance at the second end remote from the fault locator 1, instead of a representative value for the source impedance at the remote end, and by communicating that measured value to the local end using a communication means. Coefficients from (9) are determined with the local measurements and the impedance data for the transmission line, the extra link between the line terminals and the equivalent systems at the line terminals. Impedance of the equivalent system at the local substation ( $Z_{1A}$ ) can be traced on-line with the local measurements. In contrast, the remote system impedance ( $Z_{1B}$ ) is not measurable locally from A. Thus, the "representative" value of this impedance may be provided for the algorithm [1-15 2].

The alternative solution for the single line case is shown in Figure 6, which shows a fault locator 1 at the first end 2 near to system A, and another device 10 located at the remote end 3 close to system B, indicated as RD. A communication signal 9 is shown being sent from the 10 at the remote end to the fault locator 1 at the local end.

The remote source impedance ( $Z_{1B}$ ) is measured by the remote device RD, 10, which may be another fault locator or any suitable device such as a digital relay or digital fault recorder, in the remote substation and the measurement 9 is sent via a communication channel 60. Synchronization of measurements at the line terminals is not required. The source impedance is calculated from the known relation between the incremental positive voltage ( $\Delta V_{B1}$ ) and the incremental positive sequence current ( $\Delta I_{B1}$ ) [3-4]:

$$Z_{1B} = -\frac{\Delta V_{B1}}{\Delta I_{B1}} \quad (11)$$

Similarly, the fault locator 1 calculates the local source impedance

$$Z_{1sA} = -\frac{\Delta V_{A1}}{\Delta I_{A1}} \quad (11a)$$

- 5 In another and preferred embodiment of the invention, compensation is carried out for the shunt capacitance of the line. Compensation for shunt capacitances effect can be accomplished by taking into account the lumped model (only the longitudinal  $R-X_L$  parameters are taken into account) or the distributed long transmission line model. The distributed long line model [5] as providing higher accuracy of fault location, has been considered here.

- The compensation for the single line is presented further. This means that when composing fault loop current (3) the term reflecting the mutual coupling effect disappears ( $a_{0m}=0$ ). Moreover, the single subscript (A instead of AA) is used.

- Fault location procedure with compensating for shunt capacitances of a transmission line requires the following additional input data:

- $C_{1L}$  - shunt capacitance of a whole line for the positive and the negative sequences (parameters of a line for the positive and the negative sequences are identical and thus:  $C_{2L}=C_{1L}$ )  
 25  $C_{0L}$  - shunt capacitance of a whole line for the zero sequence,  
 $l$  - total line length (km), used for expressing impedances/capacitances of the line per km length.

- The compensation of shunt capacitances may be introduced while determining the voltage drop across the faulted line segment - the second term in the generalized fault loop model (1). This requires compensating the components of the calculated currents for particular sequences. Thus, the original measured



currents:  $I_{A1}$ ,  $I_{A2}$ ,  $I_{A0}$  have to be replaced by the currents after the introduced compensation:  $I_{A1\_comp}$ ,  $I_{A2\_comp}$ ,  $I_{A0\_comp}$ .

At the same time the original fault loop voltage, the first term in the model (1), is taken for a distance to fault

5 calculation. As concerns determining the voltage drop across the fault resistance, the third term in (1), it is assumed here, which is a standard practice, that the effect of line capacitances at the location of the fault (point F), may be neglected. This is justified as the impedance of the  
10 capacitive branch at that location is much greater than the fault resistance. This means that the voltage drop across the fault resistance is determined without taking into account the shunt capacitances.

15 Calculating a distance to fault the following impedances (defined below) are taken as:

$Z_{1L}^{long}$  - positive sequence impedance of a line with taking into account the distributed long line model,

$Z_{0L}^{long}$  - as above, but for the zero sequence.

20

The compensation procedure requires iterative calculations, performed until the convergence is achieved (i.e. iterated until the location estimate ceases to differ from the previous estimate). However, the studies conducted revealed that  
25 results of acceptable accuracy may be obtained using a fixed number of iterations, for example, 2-3 iterations. The calculated distance to a fault from a particular (say, present iteration) is utilized for determining the shunt current in the next iteration. The determined shunt current is then  
30 deduced from the measured current. A distance to fault, calculated without considering the shunt effect (10), is taken as the starting value for the first iteration.

A way of conducting the first iteration of the compensation is shown in Figures 7, 8, 9 for the positive sequence, negative sequence and zero sequence respectively with taking into account the shunt capacitances effect.

- 5 As a result of performing the first iteration for the positive sequence (Figure 7) the compensated current  $I_{A1\_comp\_1}$  is calculated and the last index in the subscript denotes the first iteration. The calculation is based on deducing the shunt current from the calculated positive sequence current  $I_{A1}$
- 10 calculated from the measured phase currents - Fig.2:

$$I_{A1\_comp\_1} = I_{A1} - 0.5d_v l B'_{1L} A_{\tanh l} V_{A1} \quad (12)$$

where:

$d_v$  - distance to fault calculated without taking into account the shunt capacitance effect (10),

- 15  $l$  - total line length (km)

$$A_{\tanh l} = \frac{\tanh\left(\sqrt{0.5Z'_{1L}B'_{1L}d_v l}\right)}{\sqrt{0.5Z'_{1L}B'_{1L}d_v l}}$$

$B'_{1L} = \frac{j\omega C_{1L}}{l}$  - positive sequence admittance (capacitive) of a line per km length (S/km)

$Z'_{1L} = \frac{Z_{1L}}{l}$  - positive sequence impedance of a line per km length

- 20 ( $\Omega/\text{km}$ )

Positive sequence impedance of a faulted line segment, between points A and F, without taking into account the shunt capacitances effect and considering the lumped line model equals:

$$d_v l Z'_{1L} \quad (13)$$

while for the considered here distributed long line model:

$$d_v l Z'_{1L} A_{\sinh l} \quad (14)$$

where:

$$A_{\sinh 1} = \frac{\sinh(\sqrt{Z'_{1L} B'_{1L}} d_v l)}{\sqrt{Z'_{1L} B'_{1L}} d_v l}$$

Thus, the positive sequence impedance of a line with taking  
5 into account the distributed long line model ( $Z_{1L}^{long}$ ) equals:

$$Z_{1L}^{long} = A_{\sinh 1} Z_{1L} \quad (15)$$

As a result of performing the first iteration for the negative  
sequence (Figure 8) the compensated current  $I_{A2\_comp\_1}$  is  
10 calculated and the last index in the subscript denotes the  
first iteration. This is based on deducing the shunt current  
from the calculated negative sequence current  $I_{A2}$ , calculated  
from the measured phase currents - Fig.2):

$$I_{A2\_comp\_1} = I_{A2} - 0.5 d_v l B'_{2L} A_{\tanh 2} V_{A2} \quad (16)$$

15

where, taking into account that the line parameters for the  
positive and for the negative sequences are identical

$$(C_{2L} = C_{1L}, Z_{2L} = Z_{1L}):$$

$$A_{\tanh 2} = A_{\tanh 1}$$

20

$$B'_{2L} = B'_{1L}$$

As a result of performing the first iteration for the zero  
sequence (Figure 9) the compensated current  $I_{A0\_comp\_1}$  is  
calculated, last index in the subscript denotes the first  
25 iteration. This calculation is based on deducing the shunt  
current from the calculated zero sequence current  $I_{A0}$ ,  
calculated from the measured phase currents - Fig.2:

$$I_{A0\_comp\_1} = I_{A0} - 0.5 d_v l B'_{0L} A_{\tanh 0} V_{A0} \quad (17)$$

where:

$$A_{\sinh 0} = \frac{\tanh\left(\sqrt{0.5Z'_{0L}B'_{0L}d_v l}\right)}{\sqrt{0.5Z'_{0L}B'_{0L}d_v l}}$$

$B'_{0L} = \frac{j\omega C_{0L}}{l}$  - zero sequence admittance (capacitive) of a line per km length (S/km)

$Z'_{0L} = \frac{Z_{0L}}{l}$  - zero sequence impedance of a line per km length

5 ( $\Omega/\text{km}$ )

Zero sequence impedance of a faulted line segment, that is between points A and F, without taking into account the shunt capacitances effect and considering the lumped line model equals:

$$d_v l Z'_{0L} \quad (18)$$

while for the considered here distributed long line model:

$$d_v l Z'_{0L} A_{\sinh 0} \quad (19)$$

15 where:

$$A_{\sinh 0} = \frac{\sinh\left(\sqrt{Z'_{0L}B'_{0L}d_v l}\right)}{\sqrt{Z'_{0L}B'_{0L}d_v l}}$$

Thus, the zero sequence impedance of a line with taking into account the distributed long line model ( $Z_{0L}^{long}$ ) equals:

$$Z_{0L}^{long} = A_{\sinh 0} Z_{0L} \quad (20)$$

The quadratic complex formula (8) with two unknowns

( $d_{comp\_1}$  [p.u.] - sought fault distance,  $R_F$  - fault resistance)

after introducing the compensation (first iteration) takes the following form:

$$K_1 Z_{1L}^{long} (d_{comp\_1})^2 + (L_1 Z_{1L}^{long} - K_1 Z_{A-p}^{comp-1}) d_{comp\_1} - L_1 Z_{A-p}^{comp-1} + R_F M_1 \frac{(a_{F1} \Delta I_{AA1} + a_{F2} I_{AA2})}{I_{A-p}^{comp-1}} = 0 \quad (21)$$

where:

$$\underline{Z}_{1L}^{long} = \underline{A}_{sinh1} \underline{Z}_{1L}, \quad \underline{A}_{sinh1} = \frac{\sinh\left(\sqrt{\underline{Z}_{1L}' \underline{B}_{1L}'} d_{vl}\right)}{\sqrt{\underline{Z}_{1L}' \underline{B}_{1L}'} d_{vl}},$$

$$\underline{Z}_{A-p}^{comp-1} = \frac{\underline{V}_{A-p}}{\underline{I}_{A-p}^{comp-1}} - \text{fault loop impedance calculated with:}$$

$\underline{V}_{A-p}$  - original fault loop voltage (2),

$$\underline{I}_{A-p}^{comp-1} = \underline{a}_1 \underline{I}_{A1\_comp-1} + \underline{a}_2 \underline{I}_{A2\_comp-1} + \underline{a}_0 \underline{I}_{A0\_comp-1} - \text{fault loop current}$$

composed of the positive (12), negative (16) and zero (17) sequence currents obtained after deducing the respective capacitive currents from the original currents.

10 Writing (21) in a more compact form results in:

$$\underline{A}_2^{comp-1} (d_{comp-1})^2 + \underline{A}_1^{comp-1} d_{comp-1} + \underline{A}_0^{comp-1} + \underline{A}_{00}^{comp-1} R_F = 0 \quad (21a)$$

where:

$$\underline{A}_2^{comp-1} = \underline{A}_{2\_Re}^{comp-1} + j \underline{A}_{2\_Im}^{comp-1} = \underline{K}_1 \underline{Z}_{1L}^{long}$$

$$\underline{A}_1^{comp-1} = \underline{A}_{1\_Re}^{comp-1} + j \underline{A}_{1\_Im}^{comp-1} = \underline{L}_1 \underline{Z}_{1L}^{long} - \underline{K}_1 \underline{Z}_{A-p}^{comp-1}$$

$$15 \quad \underline{A}_0^{comp-1} = \underline{A}_{0\_Re}^{comp-1} + j \underline{A}_{0\_Im}^{comp-1} = -\underline{L}_1 \underline{Z}_{A-p}^{comp-1}$$

$$\underline{A}_{00}^{comp-1} = \underline{A}_{00\_Re}^{comp-1} + j \underline{A}_{00\_Im}^{comp-1} = \frac{\underline{M}_1 (\underline{a}_{F1} \underline{\Delta I}_{AA1} + \underline{a}_{F2} \underline{\Delta I}_{AA2})}{\underline{I}_{A-p}^{comp-1}}$$

$$\underline{Z}_{A-p}^{comp-1} = \frac{\underline{V}_{A-p}}{\underline{I}_{A-p}^{comp-1}} - \text{fault loop impedance calculated with:}$$

$\underline{V}_{A-p}$  - original (uncompensated) fault loop voltage (2),

$$\underline{I}_{A-p}^{comp-1} = \underline{a}_1 \underline{I}_{A1\_comp-1} + \underline{a}_2 \underline{I}_{A2\_comp-1} + \underline{a}_0 \underline{I}_{A0\_comp-1} - \text{fault loop current}$$

20 composed of the positive (12), negative (16) and zero (17) sequence currents obtained after deducing the respective capacitive currents from the original currents,

$\underline{K}_1$ ,  $\underline{L}_1$ ,  $\underline{M}_1$  - coefficients gathered in TABLE 3.

Formula (21a) can be written down separately for real and  
25 imaginary parts:

$$A_{2\_Re}^{comp-1}(d_{comp-1})^2 + A_{1\_Re}^{comp-1}d_{comp-1} + A_{0\_Re}^{comp-1} + A_{00\_Re}^{comp-1}R_F = 0 \quad (21b)$$

$$A_{2\_Im}^{comp-1}(d_{comp-1})^2 + A_{1\_Im}^{comp-1}d_{comp-1} + A_{0\_Im}^{comp-1} + A_{00\_Im}^{comp-1}R_F = 0 \quad (21c)$$

5 Combining (21b) and (21c) in such the way that fault resistance is eliminated, that is, equation (21b) is multiplied by  $A_{00\_Im}^{comp-1}$  and equation (21c) by  $A_{00\_Re}^{comp-1}$  and then subtracting them, yields the quadratic formula for a sought fault distance:

$$10 \quad B_2^{comp-1}(d_{comp-1})^2 + B_1^{comp-1}d_{comp-1} + B_0^{comp-1} = 0 \quad (22)$$

where:

$$B_2^{comp-1} = A_{2\_Re}^{comp-1}A_{00\_Im}^{comp-1} - A_{2\_Im}^{comp-1}A_{00\_Re}^{comp-1}$$

$$B_1^{comp-1} = A_{1\_Re}^{comp-1}A_{00\_Im}^{comp-1} - A_{1\_Im}^{comp-1}A_{00\_Re}^{comp-1}$$

$$B_0^{comp-1} = A_{0\_Re}^{comp-1}A_{00\_Im}^{comp-1} - A_{0\_Im}^{comp-1}A_{00\_Re}^{comp-1}$$

15

Equation (22) has two roots  $[(d_{comp-1})_1, (d_{comp-1})_2]$  for the distance to fault:

$$(d_{comp-1})_1 = \frac{-B_1^{comp-1} - \sqrt{(B_1^{comp-1})^2 - 4B_2^{comp-1}B_0^{comp-1}}}{2B_2^{comp-1}} \quad (23)$$

$$(d_{comp-1})_2 = \frac{-B_1^{comp-1} + \sqrt{(B_1^{comp-1})^2 - 4B_2^{comp-1}B_0^{comp-1}}}{2B_2^{comp-1}}$$

20 The root, which corresponds to the selected previously root (10) for  $d$  (uncompensated) is taken as the valid result. The compensation procedure requires iterative calculations, performed until the convergence is achieved (i.e. until the location estimates cease to change from the previous estimates) or as with a fixed number of iterations such as 2-3 iterations. The calculated distance to a fault from a particular (say, present iteration) is utilized for determining the shunt current in the next iteration.

The method of the invention is illustrated in two flow-charts of the FL algorithm, Figure 10, single line and in Figure 11, parallel lines.

5

As shown in the flowchart in Figure 10 the following measurements are utilized:

- voltages from the side A from particular phases  $a, b, c$ :

$V_{A_a}, V_{A_b}, V_{A_c}$

10 - currents from the side A from particular phases  $a, b, c$ :  $i_{A_a}, i_{A_b}, i_{A_c}$

The input data utilized at step 101, Input data and measurements, are as follows:

- impedances of the line for the positive ( $Z_{1L}$ ) and zero ( $Z_{0L}$ ) sequences,

15

- impedance of the extra link 25, 45, 55 between the substations A, B for the positive (negative) sequences ( $Z_{1AB}$ )

- source impedances for the positive (negative) sequences ( $Z_{1sA}, Z_{1sB}$ ): the representative values or the measured values

20

are used, and a communication means is used for sending the measured remote source impedance as previously described,

- information on the fault type (from the protective relay).

25

The measured fault quantities (voltages and currents) undergo adaptive filtering at step 104, Adaptive filtering of phase quantities, aimed at rejecting the dc components from currents and the transients induced by Capacitive Voltage Transformers (CVTs).

30

In the next step the symmetrical components of voltages and currents are calculated, step 105, which is equivalent to that as shown in Figures 3a, 3b. The fault loop signals are

composed: the fault loop voltage as in (2), while the fault loop current as in (3) - but with taking  $a_{0m}=0$ .

The distance to fault without taking into account the shunt capacitances effect ( $d$ ) is calculated at step 106 by solving the quadratic formula (9). The solution of (9) is presented in (10).

The result obtained without taking into account the shunt capacitances effect  $d$  following 106 is treated as the starting value for performing the compensation for shunt capacitances. The distributed long line model is applied for the compensation.

The following additional data is required for the calculating the compensation for shunt capacitance, step 107:

- positive sequence capacitance of the line ( $C_{1L}$ )
- zero sequence capacitance of the line ( $C_{0L}$ )
- line length ( $l$ ), which is used to express line impedances / capacitances per km length.

The first iteration of the compensation leads to the quadratic equation (22), which is solved in (23). The next iterations are performed analogously. Iterative calculations are performed until the convergence is achieved or a fixed number of iterations, i.e. 2-3 iterations, may be made. The calculated distance to a fault from a particular (say, present iteration) is utilized for determining the shunt current in the next iteration. After completing the iterative calculations the distance to fault  $d_{comp}$  is obtained.

As shown in the flowchart in Figure 11 for parallel lines the following measurements are utilized:

- voltages from the side A and line LA from particular phases  $a, b, c$ :  $v_{AA\_a}$ ,  $v_{AA\_b}$ ,  $v_{AA\_c}$



- currents from the side A and line LA from particular phases  
a, b, c:  $i_{AA\_a}$ ,  $i_{AA\_b}$ ,  $i_{AA\_c}$
- zero sequence current from the healthy parallel line LB:  $i_{AB0}$

- 5 The input data utilized in step 111, Input data and measurements, are as follows:
- impedances of the faulted line for the positive ( $Z_{1LA}$ ) and zero ( $Z_{0LA}$ ) sequences
  - impedance of the healthy line for the positive (negative) sequence ( $Z_{1LB}$ )
  - impedance of the extra link between the substations A, B for the positive (negative) sequences  
( $Z_{1AB}$ )
  - zero sequence impedance for the mutual coupling ( $Z_{0m}$ )
  - 15 - representative values of the source impedances for the positive (negative) sequences  
( $Z_{1sA}$ ,  $Z_{1sB}$ )
  - information on the fault type is obtained from the protective relay.

- 20 The measured fault quantities, the voltages and currents; undergo adaptive filtering at step 114 for the purpose of rejecting the dc components from currents and the transients induced by Capacitive Voltage Transformers (CVTs).

25 In the next step 115 the symmetrical components of voltages and currents are calculated as shown in Figures 3a, 3b. The fault loop signals are composed: the fault loop voltage as in (2), while the fault loop current as in (3) - but with taking  $u_{0m} = u_0$ .

30 The distance to fault without taking into account the shunt capacitances effect ( $d$ ) is calculated in step 116 by solving

the quadratic formula (9). The solution of (9) is presented in (10).

The result obtained without taking into account the shunt capacitances effect ( $d$ ) is treated as the starting value for performing the compensation for shunt capacitances. The distributed long line model is applied for the compensation.

The following additional data for the faulted line is required for the compensation for shunt capacitances step 117:

- positive sequence capacitance of the line ( $C_{1L}$ )
- 10 - zero sequence capacitance of the line ( $C_{0L}$ )
- line length ( $l$ ), which is used to express line impedances / capacitances per km length.

In the case of a single line, the compensation is performed analogously. Iterative calculations are performed until the convergence is achieved or by using a fixed number of iterations, e.g. 2-3 iterations. The calculated distance to a fault from a particular iteration, for example the present iteration, is utilized for determining the shunt current in the next iteration. After completing the iterative calculations the distance to fault  $d_{comp}$  is obtained.

Figure 18 shows an embodiment of a device for determining the distance from one end, here shown as end A of a section of power transmission or distribution line A-B, to a fault F on the power line according to the method of the invention described. The fault locator device 1 receives measurements from measuring devices located at one end A such as current measuring means 14, and voltage measurements from voltage measurement means 11. The fault locator device may comprise measurement value converters, members for treatment of the calculating algorithms of the method, indicating means for the calculated distance to fault and a printer or connection to a printer or facsimile machine or similar for printout of the

calculated fault. In a preferred embodiment of the device the fault locator comprises computer program means for providing a display of the information provided by the method of the invention, such as distance to a fault  $d$  or  $d_{comp}$  on a terminal  
 5 which may be remote from the location of the line and/or the fault locator. Preferably the computer program means receives information from the fault locator and makes it available to provide information for a display of a computer such that an operator or engineer may see a value for the calculated  
 10 distance to a fault displayed. The value may be displayed relative to a schematic display of the line or network in which the fault has taken place.

In the embodiment shown, measuring device 14 for continuous  
 15 measurement of all the phase currents, and measuring device 11 for measurement of voltages are arranged at one end, station A. Optionally, measuring devices such as 15, 13 may also be arranged at station B but they are not necessary to practise the invention. The measured values such as: the three-phase  
 20 voltages ( $V_{AA}$ ) of the faulted line, three-phase voltages ( $\underline{I}_{AA}$ ) of the faulted line and zero sequence current ( $\underline{I}_{AB0}$ ) from the healthy parallel line (note that zero sequence is not present when the single line only is considered), and a value representative of the source impedance at B,  $Z_{1sB}$  as a are all  
 25 passed to a calculating unit comprised in fault locator 1, filtered such as described in relation to figure 3a, 3b, and stored in memory means. The calculating unit is provided with the calculating algorithms described, and programmed for the processes needed for calculating the distance to fault.  
 30 Optionally, the source impedance for the remote end,  $Z_{1sB}$  may be measured by the remote device RD, 10, and the information sent via a high speed communication means 60 to the fault locator at A. In some applications it will be preferable to use a measured value sent from B instead of a representative  
 35 value stored at A. It may be seen in Figure 18 that current

measuring means 15 and voltage measuring means 13 at remote end B may provide a RD 10, a fault locator, or any suitable device with measurements to calculate the remote source impedance

5

The calculating unit of fault locator 1 is provided with pre-fault phase currents and also with known values such as shunt capacitances and the impedances of the line. In respect of the occurrence of a fault, information regarding the type of fault, phase-to-phase, phase-to-ground etc., may be supplied to the calculating unit of the fault locator. When the calculating unit has determined the distance to fault, it is displayed on the device and/or sent to display means which may be remotely located. A printout or fax of the result may also be provided. In addition to signalling the fault distance, the device can produce reports, in which are recorded measured values of the currents of both lines, voltages, type of fault and other measured and/or calculated information associated with a given fault at a distance.

20

The method and a fault locator device according to any embodiment of the invention may be used to determine distance to a fault on a section of power transmission line. The present invention may also be used to determine a distance to a fault on a section of a power distribution line, or any other line or bus arranged for any of generation, transmission, distribution, control or consumption of electrical power.

30

The fault locator device and system may comprise filters for filtering the signals, converters for sampling the signals and one or more micro computers. The microprocessor (or processors) comprises a central processing unit CPU performing the steps of the method according to the invention. This is performed with the aid of a computer program, which is stored

35

in the program memory. It is to be understood that the computer program may also be run on one or more general purpose industrial computers or microprocessors instead of a specially adapted computer.

5

The computer program comprises computer program code elements or software code portions that make the computer perform the method using equations, algorithms, data and calculations previously described. A part of the program may be stored in a processor as above, but also in a ROM, RAM, PROM or EPROM chip or similar. The program in part or in whole may also be stored on, or in, other suitable computer readable medium such as a magnetic disk, CD-ROM or DVD disk, hard disk, magneto-optical memory storage means, in volatile memory, in flash memory, as  
10 firmware, or stored on a data server.  
15

A computer program according to the invention may be stored at least in part on different mediums that are computer readable. Archive copies may be stored on standard magnetic disks, hard  
20 drives, CD or DVD disks, or magnetic tape. The databases and libraries are stored preferably on one or more local or remote data servers, but the computer program and/or computer program product may, for example at different times, be stored in any of; a volatile Random Access memory (RAM) of a computer or  
25 processor, a hard drive, an optical or magneto-optical drive, or in a type of non-volatile memory such as a ROM, PROM, or EPROM device. The computer program may also be arranged in part as a distributed application capable of running on several different computers or computer systems at more or  
30 less the same time.

In another preferred embodiment, the fault locator may be used with parallel lines to locate single phase-to-ground faults (a-g, b-g, c-g faults) in case of unavailability of

measurements from the healthy parallel line. Two modes of the healthy line operation are taken into account:

- the healthy line being in operation,
- the healthy line switched-off and grounded at both the ends.

5 Figure 19 shows the flow-chart of the algorithm for locating faults in parallel lines under unavailability of measurements from the healthy parallel line. The unavailable, here healthy line zero sequence current, is required for reflecting the mutual coupling effect under single phase-to-ground faults (a-g, b-g, c-g faults). The unavailable current is thus  
10 estimated. The other faults can be located with the standard fault location algorithm (such as the algorithm from reference [1]).

The sequence of computations for the presented one-end fault  
15 location algorithm is as follows.

As shown in the flowchart in Figure 19 the following measurements are utilized:

- voltages from the side A and line LA (faulted) from particular phases:  $v_{AA\_a}$  ,  $v_{AA\_b}$  ,  $v_{AA\_c}$
- 20 - currents from the side A and line LA (faulted) from particular phases:  $i_{AA\_a}$  ,  $i_{AA\_b}$  ,  $i_{AA\_c}$

The utilized input data are as follows:

- impedances of the faulted line for the positive ( $Z_{1LA}$ ) and zero ( $Z_{0LA}$ ) sequences
- 25 - impedance of the healthy line for the zero sequence ( $Z_{0LB}$ )
- zero sequence impedance for the mutual coupling ( $Z_{0m}$ )
- information on the fault type (from the protective relay)
- mode of the healthy line operation: in operation or switched-off and grounded at both ends).

30 The measured fault quantities (voltages and currents) undergo adaptive filtering aimed at rejecting the dc components from

currents and the transients induced by Capacitive Voltage Transformers (CVTs).

The following equations are used in the method of the present parallel line embodiment. In addition to the algorithm (1) described above for estimating distance to fault ( $d$  [pu]) by considering the Kirchhoff's voltage law for the fault loop as seen from the locator installation point:

$$\underline{V}_{AA\_p} - d\underline{Z}_{1LA}\underline{I}_{AA\_p} - R_F \left( \frac{\underline{a}_{F1}\underline{I}_{AA1}}{\underline{k}_{F1}} + \frac{\underline{a}_{F2}\underline{I}_{AA2}}{\underline{k}_{F2}} + \frac{\underline{a}_{F0}\underline{I}_{AA0}}{\underline{k}_{F0}} \right) = 0 \quad (24)$$

the fault loop voltage ( $\underline{V}_{AA\_p}$ ) and current ( $\underline{I}_{AA\_p}$ ) can be expressed in terms of the symmetrical quantities:

$$\underline{V}_{AA\_p} = \underline{a}_1\underline{V}_{AA1} + \underline{a}_2\underline{V}_{AA2} + \underline{a}_0\underline{V}_{AA0} \quad (2)$$

and

$$\underline{I}_{AA\_p} = \underline{I}_{AA\_p}^{SL} + \frac{\underline{Z}_{0m}}{\underline{Z}_{1LA}} \underline{I}_{AB0} \quad (25)$$

where:

$$\underline{I}_{AA\_p}^{SL} = \underline{a}_1\underline{I}_{AA1} + \underline{a}_2\underline{I}_{AA2} + \underline{a}_0 \frac{\underline{Z}_{0LA}}{\underline{Z}_{1LA}} \underline{I}_{AA0} \quad (25a)$$

is the fault loop current without compensating for the mutual coupling effect (i.e. composed as for the single line - superscript  $SL$ ),

$\underline{a}_1, \underline{a}_2, \underline{a}_0$  - complex coefficients gathered in TABLE 1 (the derivation as in APPENDIX A1),

$\underline{V}_{AA1}, \underline{V}_{AA2}, \underline{V}_{AA0}$  - positive, negative and zero sequence of measured voltages,

$\underline{I}_{AA1}, \underline{I}_{AA2}, \underline{I}_{AA0}$  - positive, negative and zero sequence current from the faulted line  $LA$ ,

$\underline{I}_{AB0}$  - unavailable zero sequence current from the healthy parallel line  $LB$  (to be estimated),

$Z_{1LA}$ ,  $Z_{0LA}$  - positive and zero sequence impedance the whole line LA,

$Z_{0m}$  - zero sequence impedance for mutual coupling between the lines LA and LB,

5  $R_F$  - unknown fault resistance.

In the next step the symmetrical components of voltages and currents are calculated as shown in Figures 3a, 3b. The fault loop signals are composed: the fault loop voltage as in (2), while the fault loop current as in (25). The formulae (2)-(25)

10 present the fault loop signals expressed in terms of the symmetrical components of the measured signals. However, one can use the classic way for composing the fault loop signals.

The presented method covers single phase-to ground faults (a-  
15 g, b-g, c-g faults). The other remaining faults have to be located with the fault location algorithms described above or standard fault location algorithm [1]. Distance to fault ( $d$ ) for the considered here single phase-to ground faults is calculated by solving the quadratic formula for a sought  
20 distance to fault (26). Equation (26) is the same as equation (10) except that the values for  $B_1, B_2, B_3$  are different to the values determined in (10). The solution gives two roots:

$$\begin{aligned} d_1 &= \frac{-B_1 - \sqrt{B_1^2 - 4B_2B_0}}{2B_2} \\ d_2 &= \frac{-B_1 + \sqrt{B_1^2 - 4B_2B_0}}{2B_2} \end{aligned} \quad (26)$$

(as above, the root which fulfils the condition ( $0 \leq d \leq 1$ ) is  
25 selected as the solution for the distance to fault). One has to substitute the following into (26):

$$B_2 = \text{real}(\underline{A}_2) \text{imag}(\underline{A}_{00}) - \text{real}(\underline{A}_{00}) \text{imag}(\underline{A}_2)$$

$$B_1 = \text{real}(\underline{A}_1) \text{imag}(\underline{A}_{00}) - \text{real}(\underline{A}_{00}) \text{imag}(\underline{A}_1)$$



$$B_0 = \text{real}(\underline{A}_0) \text{imag}(\underline{A}_{00}) - \text{real}(\underline{A}_{00}) \text{imag}(\underline{A}_0)$$

where:

$$\underline{A}_2 = -\underline{Z}_{1LA} \underline{K}_1 \underline{I}_{AA\_p}^{SL} - \frac{\underline{Z}_{0m}}{\underline{P}_0} \underline{K}_1 \underline{I}_{AA0} - \frac{\underline{Z}_{0m}}{\underline{P}_0} \underline{Q}_0$$

$$\underline{A}_1 = \underline{K}_1 \underline{V}_{AA\_p} - \underline{Z}_{1LA} \underline{L}_1 \underline{I}_{AA\_p}^{SL} - \frac{\underline{Z}_{0m}}{\underline{P}_0} \underline{L}_1 \underline{I}_{AA0}$$

$$5 \quad \underline{A}_0 = \underline{L}_1 \underline{V}_{AA\_p}$$

$$\underline{A}_{00} = -\underline{M}_1 \underline{a}_{F2} \underline{I}_{AA2}$$

$$\underline{Q}_0 = \underline{M}_1 (\underline{b}_{F1} \underline{I}_{AA1} + \underline{b}_{F2} \underline{I}_{AA2})$$

The recommended SET of the coefficients  $\underline{b}_{F1}, \underline{b}_{F2}$  are taken from

10 TABLE 4 and the recommended SET of the coefficients  $\underline{a}_{F1}, \underline{a}_{F2}, \underline{a}_{F0}$  from TABLE 5.

Fault loop voltage in this embodiment is found from the TABLE below

Fault loop voltage composed in terms of symmetrical components	Fault loop voltage composed as in the classic approach
$\underline{V}_{AA\_p} = \underline{a}_1 \underline{V}_{AA1} + \underline{a}_2 \underline{V}_{AA2} + \underline{a}_0 \underline{V}_{AA0}$ a-g fault: $\underline{a}_1 = \underline{a}_2 = \underline{a}_0 = 1$ b-g fault: $\underline{a}_1 = \underline{a}^2, \underline{a}_2 = \underline{a}, \underline{a}_0 = 1$ c-g fault: $\underline{a}_1 = \underline{a}, \underline{a}_2 = \underline{a}^2, \underline{a}_0 = 1$ $\underline{a} = \exp(j2\pi/3), \quad j = \sqrt{-1}$	a-g fault: $\underline{V}_{AA\_p} = \underline{V}_{AA\_a}$ a-g fault: $\underline{V}_{AA\_p} = \underline{V}_{AA\_b}$ a-g fault: $\underline{V}_{AA\_p} = \underline{V}_{AA\_c}$

15

Fault loop current  $\underline{I}_{AA\_p}^{SL}$  composed as for the single line is found from the TABLE below

$I_{AA\_p}^{SL}$ in terms of symmetrical components	$I_{AA\_p}^{SL}$ as in the classic approach
$I_{AA\_p}^{SL} = a_1 I_{AA1} + a_2 I_{AA2} + a_0 \frac{Z_{0LA}}{Z_{1LA}} I_{AA0}$ <p>a-g fault: <math>a_1 = a_2 = a_0 = 1</math></p> <p>b-g fault: <math>a_1 = a^2, a_2 = a, a_0 = 1</math></p> <p>c-g fault: <math>a_1 = a, a_2 = a^2, a_0 = 1</math></p> <p><math>a = \exp(j2\pi/3), j = \sqrt{-1}</math></p>	<p>a-g fault: <math>I_{AA\_p}^{SL} = I_{AA\_a} + k_0 I_{AA0}</math></p> <p>a-g fault: <math>I_{AA\_p}^{SL} = I_{AA\_a} + k_0 I_{AA0}</math></p> <p>a-g fault: <math>I_{AA\_p}^{SL} = I_{AA\_a} + k_0 I_{AA0}</math></p> <p>where: <math>k_0 = \frac{Z_{0LA} - Z_{1LA}}{Z_{1LA}}</math></p>

5 The complex coefficients dependent on the mode of the healthy parallel line operation:

a) healthy line LB is in operation:

$$P_0 = \frac{Z_{0LB} - Z_{0m}}{Z_{0LA} - Z_{0m}}$$

$$K_1 = -Z_{1LA}(Z_{1sA} + Z_{1sB} + Z_{1LB})$$

$$L_1 = -K_1 + Z_{1LB}Z_{1sB}$$

10

$$M_1 = Z_{1LA}Z_{1LB} + Z_{1LA}(Z_{1sA} + Z_{1sB}) + Z_{1LB}(Z_{1sA} + Z_{1sB})$$

b) healthy line LB is switched-off and grounded:

$$P_0 = -\frac{Z_{0LB}}{Z_{0m}}$$

$$K_1 = -Z_{1LA}$$

$$L_1 = Z_{1LA} + Z_{1sB}$$

$$M_1 = Z_{1sA} + Z_{1sA} + Z_{1LA}$$

15

In another embodiment, which is presented here, the method is applied for the standard availability of the measured signals and is only valid for ground faults, including both:

- single phase-to-ground faults
- 5 - phase-to-phase-to-ground faults.

Fault location procedures obtained for these faults under the standard availability of the fault locator input signals are extremely simple and compact. Distance to a fault is calculated with a first order formula.

10 Figure 22 shows a flow-chart of the developed algorithm for locating ground faults in parallel transmission lines.

The sequence of computations for the presented one-end fault locator is as follows. As shown in the flowchart in Figure 22.B3 the following measurements are utilized:

- 15 - voltages from the side A and line LA from particular phases a, b, c:  $V_{AA\_a}$ ,  $V_{AA\_b}$ ,  $V_{AA\_c}$
- currents from the side A and line LA from particular phases a, b, c:  $i_{AA\_a}$ ,  $i_{AA\_b}$ ,  $i_{AA\_c}$
- zero sequence current from the healthy parallel line LB:  $i_{AB0}$

20 The utilized input data are as follows:

- impedances of the faulted line for the positive ( $Z_{ILA}$ ) and zero ( $Z_{0LA}$ ) sequences,
- impedance of the healthy line for the zero sequence ( $Z_{0LB}$ )
- zero sequence impedance for the mutual coupling ( $Z_{0m}$ )
- 25 - information on the fault type from the protective relay.

The measured fault quantities (voltages and currents) undergo adaptive filtering aimed at rejecting the dc components from currents and the transients induced by Capacitive Voltage Transformers (CVTs), preferably as described and shown in relation to Figures 3a, 3b.

30

A generalized model of the fault loop, used for deriving the algorithm of the present embodiment, is stated as follows:

$$\underline{V}_{AA\_p} - d \underline{Z}_{ILA} \underline{I}_{AA\_p} - R_F (\underline{a}_{F1} \underline{I}_{F1} + \underline{a}_{F2} \underline{I}_{F2} + \underline{a}_{F0} \underline{I}_{F0}) = 0 \quad (1)$$

where:

- 5  $d$  - unknown and sought distance to fault,  
 $\underline{Z}_{ILA}$  - positive sequence impedance of the faulted line,  
 $\underline{V}_{AA\_p}, \underline{I}_{AA\_p}$  - fault loop voltage and current composed according to the fault type,  
 $R_F$  - fault resistance,  
 10  $\underline{I}_{Fi}$  - sequence components of the total fault current ( $i=0$  - zero sequence,  $i=1$  positive sequence,  $i=2$  - negative sequence),  
 $\underline{a}_{Fi}$  - weighting coefficients (TABLE 2).

15 Fault loop voltage and current can be expressed as in classic distance protection technique or, as in this document, in terms of the local measurements and with using the coefficients ( $\underline{a}_0, \underline{a}_1, \underline{a}_2$ ) which are gathered in TABLE 1 (derivation of the coefficients is contained in APPENDIX APP.1):

$$20 \quad \underline{V}_{AA\_p} = \underline{a}_1 \underline{V}_{AA1} + \underline{a}_2 \underline{V}_{AA2} + \underline{a}_0 \underline{V}_{AA0} \quad (2)$$

$$\underline{I}_{AA\_p} = \underline{a}_1 \underline{I}_{AA1} + \underline{a}_2 \underline{I}_{AA2} + \underline{a}_0 \frac{\underline{Z}_{0LA}}{\underline{Z}_{1LA}} \underline{I}_{AA0} + \underline{a}_0 \frac{\underline{Z}_{0m}}{\underline{Z}_{1LA}} \underline{I}_{AB0} \quad (3)$$

where:

AA, AB - subscripts used for indicating measurements acquired from the faulted line (AA) and from the healthy line (AB),  
 25 respectively,

$\underline{Z}_{0LA}, \underline{Z}_{0m}$  - impedance of the faulted line and mutual coupling between the lines for the zero sequence, respectively.

In the next step the symmetrical components of voltages and currents are calculated as shown in Figure 3a, 3b. The fault

loop signals are composed: the fault loop voltage as in (2), while the fault loop current as in (3). The formulae (2)-(3) present the fault loop signals expressed in terms of the symmetrical components of the measured signals. However, one  
 5 may instead use the classic way for composing the fault loop signals, as shown in APPENDIX A1.

The presented method covers single phase-to ground faults (a-g, b-g, c-g faults) and phase-to-phase-to-ground faults (a-b-g, b-c-g, c-a-g faults), thus, the faults for which the  
 10 highest fault resistance can be expected. The other remaining faults have to be located with the fault location algorithms described above or a standard fault location algorithm, such as for example the fault locator from reference [1]).

Distance to fault ( $d$ ) for single phase-to-ground faults is  
 15 calculated as follows:

a-g fault:

$$d = \frac{\text{imag}\{V_{AA\_p}[3(I_{AA0} - P_0 I_{AB0})]^*\}}{\text{imag}\{(Z_{1LA} I_{AA\_p})[3(I_{AA0} - P_0 I_{AB0})]^*\}} \quad (27a)$$

where:

Fault loop signals composed in terms of symmetrical components	Fault loop signals composed as in the classic approach
$V_{AA\_p} = a_1 V_{AA1} + a_2 V_{AA2} + a_0 V_{AA0}$	$V_{AA\_p} = V_{AA\_a}$
$I_{AA\_p} = a_1 I_{AA1} + a_2 I_{AA2} + a_0 \frac{Z_{0LA}}{Z_{1LA}} I_{AA0} + a_0 \frac{Z_{0m}}{Z_{1LA}} I_{AB0}$	$I_{AA\_p} = I_{AA\_a} + k_0 I_{AA0} + k_{0m} I_{AB0}$
where: $a_1 = a_2 = a_0 = 1$	where: $k_0 = \frac{Z_{0LA} - Z_{1LA}}{Z_{1LA}}$ , $k_{0m} = \frac{Z_{0m}}{Z_{1LA}}$

$$P_0 = \frac{Z_{0LB} - Z_{0m}}{Z_{0LA} - Z_{0m}} \quad (\text{for symmetrical lines: } P_0 = 1).$$

b-g fault:

$$d = \frac{\text{imag}\{V_{AA\_p}[3a^2(I_{AA0} - P_0 I_{AB0})]^*\}}{\text{imag}\{(Z_{1LA} I_{AA\_p})[3a^2(I_{AA0} - P_0 I_{AB0})]^*\}} \quad (27b)$$

where:

Fault loop signals composed in terms of symmetrical components	Fault loop signals composed as in the classic approach
$V_{AA\_p} = a_1 V_{AA1} + a_2 V_{AA2} + a_0 V_{AA0}$	$V_{AA\_p} = V_{AA\_b}$
$I_{AA\_p} = a_1 I_{AA1} + a_2 I_{AA2} + a_0 \frac{Z_{0LA}}{Z_{1LA}} I_{AA0} + a_0 \frac{Z_{0m}}{Z_{1LA}} I_{AB0}$	$I_{AA\_p} = I_{AA\_b} + k_0 I_{AA0} + k_{0m} I_{AB0}$
where: $a_1 = a^2$ , $a_2 = a$ , $a_0 = 1$ $a = \exp(j2\pi/3)$	where: $k_0 = \frac{Z_{0LA} - Z_{1LA}}{Z_{1LA}}$ , $k_{0m} = \frac{Z_{0m}}{Z_{1LA}}$

$$P_0 = \frac{Z_{0LB} - Z_{0m}}{Z_{0LA} - Z_{0m}} \quad (\text{for symmetrical lines: } P_0 = 1).$$

5 c-g fault:

$$d = \frac{\text{imag}\{V_{AA\_p}[3a(I_{AA0} - P_0 I_{AB0})]^*\}}{\text{imag}\{(Z_{1LA} I_{AA\_p})[3a(I_{AA0} - P_0 I_{AB0})]^*\}} \quad (27c)$$

where:

Fault loop signals composed in terms of symmetrical components	Fault loop signals composed as in the classic approach
$V_{AA\_p} = a_1 V_{AA1} + a_2 V_{AA2} + a_0 V_{AA0}$	$V_{AA\_p} = V_{AA\_c}$
$I_{AA\_p} = a_1 I_{AA1} + a_2 I_{AA2} + a_0 \frac{Z_{0LA}}{Z_{1LA}} I_{AA0} + a_0 \frac{Z_{0m}}{Z_{1LA}} I_{AB0}$	$I_{AA\_p} = I_{AA\_c} + k_0 I_{AA0} + k_{0m} I_{AB0}$
where: $a_1 = a$ , $a_2 = a^2$ , $a_0 = 1$ $a = \exp(j2\pi/3)$	where: $k_0 = \frac{Z_{0LA} - Z_{1LA}}{Z_{1LA}}$ , $k_{0m} = \frac{Z_{0m}}{Z_{1LA}}$

$$P_0 = \frac{Z_{0LB} - Z_{0m}}{Z_{0LA} - Z_{0m}} \quad (\text{for symmetrical lines: } P_0 = 1).$$

Distance to fault ( $d$ ) for phase-to-phase-to-ground faults can be calculated in two different ways, depending whether the pre-fault currents can be used or have to be avoided.

5

1. Procedure for distance to fault calculation with the use of pre-fault measurements:

a-b-g fault:

$$d = \frac{\text{imag}\{V_{AA\_p} [W(I_{AA0} - P_0 I_{AB0})]^r\}}{\text{imag}\{(Z_{ILA} I_{AA\_p}) [W(I_{AA0} - P_0 I_{AB0})]^r\}} \quad (28a)$$

10

where:  $W = \frac{AI_{AA1} + BI_{AA2}}{CI_{AA1} + DI_{AA2}}$ ,

$A=1-a^2$ ,  $B=1-a$ ,  $C=1+a^2$ ,  $D=1+a$

Fault loop signals composed in terms of symmetrical components	Fault loop signals composed as in the classic approach
$V_{AA\_p} = a_1 V_{AA1} + a_2 V_{AA2} + a_0 V_{AA0}$	$V_{AA\_p} = V_{AA\_a} - V_{AA\_b}$
$I_{AA\_p} = a_1 I_{AA1} + a_2 I_{AA2} + a_0 \frac{Z_{0LA}}{Z_{ILA}} I_{AA0} + a_0 \frac{Z_{0m}}{Z_{ILA}} I_{AB0}$	$I_{AA\_p} = I_{AA\_a} - I_{AA\_b}$
where: $a_1 = 1-a^2$ , $a_2 = 1-a$ , $a_0 = 0$ $a = \exp(j2\pi/3)$	

$P_0 = \frac{Z_{0LB} - Z_{0m}}{Z_{0LA} - Z_{0m}}$  (for symmetrical lines:  $P_0 = 1$ ).

15 b-c-g fault:

$$d = \frac{\text{imag}\{V_{AA\_p} [W(I_{AA0} - P_0 I_{AB0})]^r\}}{\text{imag}\{(Z_{ILA} I_{AA\_p}) [W(I_{AA0} - P_0 I_{AB0})]^r\}} \quad (28b)$$

where:  $\underline{W} = \frac{A\underline{I}_{AA1} + B\underline{I}_{AA2}}{C\underline{I}_{AA1} + D\underline{I}_{AA2}}$ ,  $\underline{A} = \underline{a}^2 - \underline{a}$ ,  $\underline{B} = \underline{a} - \underline{a}^2$ ,  $\underline{C} = -1$ ,  $\underline{D} = -1$

Fault loop signals composed in terms of symmetrical components	Fault loop signals composed as in the classic approach
$\underline{V}_{AA-p} = \underline{a}_1 \underline{V}_{AA1} + \underline{a}_2 \underline{V}_{AA2} + \underline{a}_0 \underline{V}_{AA0}$	$\underline{V}_{AA-p} = \underline{V}_{AA-b} - \underline{V}_{AA-c}$
$\underline{I}_{AA-p} = \underline{a}_1 \underline{I}_{AA1} + \underline{a}_2 \underline{I}_{AA2} + \underline{a}_0 \frac{\underline{Z}_{0LA}}{\underline{Z}_{1LA}} \underline{I}_{AA0} + \underline{a}_0 \frac{\underline{Z}_{0m}}{\underline{Z}_{1LA}} \underline{I}_{AB0}$	$\underline{I}_{AA-p} = \underline{I}_{AA-b} - \underline{I}_{AA-c}$
where: $\underline{a}_1 = \underline{a}^2 - \underline{a}$ , $\underline{a}_2 = \underline{a} - \underline{a}^2$ , $\underline{a}_0 = 0$ $\underline{a} = \exp(j2\pi/3)$	

$$\underline{P}_0 = \frac{\underline{Z}_{0LB} - \underline{Z}_{0m}}{\underline{Z}_{0LA} - \underline{Z}_{0m}} \quad (\text{for symmetrical lines: } \underline{P}_0 = 1).$$

c-a-g fault:

$$5 \quad d = \frac{\text{imag}\{\underline{V}_{AA-p} [\underline{W}(\underline{I}_{AA0} - \underline{P}_0 \underline{I}_{AB0})]^*\}}{\text{imag}\{(\underline{Z}_{1LA} \underline{I}_{AA-p}) [\underline{W}(\underline{I}_{AA0} - \underline{P}_0 \underline{I}_{AB0})]^*\}} \quad (28c)$$

where:

$$\underline{W} = \frac{A\underline{I}_{AA1} + B\underline{I}_{AA2}}{C\underline{I}_{AA1} + D\underline{I}_{AA2}}$$

$$\underline{A} = \underline{a} - 1, \quad \underline{B} = \underline{a}^2 - 1, \quad \underline{C} = \underline{a} + 1, \quad \underline{D} = \underline{a}^2 + 1$$

Fault loop signals composed in terms of symmetrical components	Fault loop signals composed as in the classic approach
$\underline{V}_{AA-p} = \underline{a}_1 \underline{V}_{AA1} + \underline{a}_2 \underline{V}_{AA2} + \underline{a}_0 \underline{V}_{AA0}$	$\underline{V}_{AA-p} = \underline{V}_{AA-c} - \underline{V}_{AA-a}$
$\underline{I}_{AA-p} = \underline{a}_1 \underline{I}_{AA1} + \underline{a}_2 \underline{I}_{AA2} + \underline{a}_0 \frac{\underline{Z}_{0LA}}{\underline{Z}_{1LA}} \underline{I}_{AA0} + \underline{a}_0 \frac{\underline{Z}_{0m}}{\underline{Z}_{1LA}} \underline{I}_{AB0}$	$\underline{I}_{AA-p} = \underline{I}_{AA-c} - \underline{I}_{AA-a}$
where: $\underline{a}_1 = \underline{a} - 1$ , $\underline{a}_2 = \underline{a}^2 - 1$ , $\underline{a}_0 = 0$ $\underline{a} = \exp(j2\pi/3)$	



$$P_0 = \frac{Z_{0LB} - Z_{0m}}{Z_{0LA} - Z_{0m}} \quad (\text{for symmetrical lines: } P_0 = 1).$$

2. Procedure for distance to fault calculation without the use of pre-fault measurements:

5 a-b-g fault:

$$d = \frac{\text{imag}[(V_a + V_b)(I_{AA0} - P_0 I_{AB0})^*]}{\text{imag}[Z_{1LA}(I_a + I_b + 2k_0 I_{AA0} + 2k_{0m} I_{AB0})(I_{AA0} - P_0 I_{AB0})^*]} \quad (29a)$$

b-c-g fault:

$$d = \frac{\text{imag}[(V_b + V_c)(I_{AA0} - P_0 I_{AB0})^*]}{\text{imag}[Z_{1LA}(I_b + I_c + 2k_0 I_{AA0} + 2k_{0m} I_{AB0})(I_{AA0} - P_0 I_{AB0})^*]} \quad (29b)$$

10

c-a-g fault:

$$d = \frac{\text{imag}[(V_c + V_a)(I_{AA0} - P_0 I_{AB0})^*]}{\text{imag}[Z_{1LA}(I_c + I_a + 2k_0 I_{AA0} + 2k_{0m} I_{AB0})(I_{AA0} - P_0 I_{AB0})^*]} \quad (29c)$$

$$\text{where: } k_0 = \frac{Z_{0LA} - Z_{1LA}}{Z_{1LA}}, \quad k_{0m} = \frac{Z_{0m}}{Z_{1LA}}.$$

15

It is also noted that while the above describes exemplifying embodiments of the invention, there are several variations and modifications which may be made to the disclosed solution without departing from the scope of the present invention as defined in the appended claims.

20

## Tables

TABLE 1. Coefficients for determining the fault loop voltage ( $\underline{V}_{AA\_p}$ ) and current ( $\underline{I}_{AA\_p}$ ) in terms of symmetrical components as defined in (2) and (3).

Fault type	$\underline{a}_1$	$\underline{a}_2$	$\underline{a}_0$
$a-g$	1	1	1
$b-g$	$\underline{a}^2$	$\underline{a}$	1
$c-g$	$\underline{a}$	$\underline{a}^2$	1
$a-b, a-b-g$ $a-b-c, a-b-c-g$	$1-\underline{a}^2$	$1-\underline{a}$	0
$b-c, b-c-g$	$\underline{a}^2-\underline{a}$	$\underline{a}-\underline{a}^2$	0
$c-a, c-a-g$	$\underline{a}-1$	$\underline{a}^2-1$	0
$\underline{a} = \exp(j2\pi/3), \quad j = \sqrt{-1}$			

5

TABLE 1A. Fault loop voltage ( $\underline{V}_{AA\_p}$ ) and current ( $\underline{I}_{AA\_p}$ ) expressed with using the classic approach.

Fault type	$\underline{V}_{AA\_p}$	$\underline{I}_{AA\_p}$
$a-g$	$\underline{V}_{AA\_a}$	$\underline{I}_{AA\_a} + \underline{k}_0 \underline{I}_{AA0} + \underline{k}_{0m} \underline{I}_{AB0}$
$b-g$	$\underline{V}_{AA\_b}$	$\underline{I}_{AA\_b} + \underline{k}_0 \underline{I}_{AA0} + \underline{k}_{0m} \underline{I}_{AB0}$
$c-g$	$\underline{V}_{AA\_c}$	$\underline{I}_{AA\_c} + \underline{k}_0 \underline{I}_{AA0} + \underline{k}_{0m} \underline{I}_{AB0}$
$a-b, a-b-g$ $a-b-c, a-b-c-g$	$\underline{V}_{AA\_a} - \underline{V}_{AA\_b}$	$\underline{I}_{AA\_a} - \underline{I}_{AA\_b}$
$b-c, b-c-g$	$\underline{V}_{AA\_b} - \underline{V}_{AA\_c}$	$\underline{I}_{AA\_b} - \underline{I}_{AA\_c}$
$c-a, c-a-g$	$\underline{V}_{AA\_c} - \underline{V}_{AA\_a}$	$\underline{I}_{AA\_c} - \underline{I}_{AA\_a}$
$\underline{k}_0 = \frac{\underline{Z}_{0LA} - \underline{Z}_{1LA}}{\underline{Z}_{1LA}}, \quad \underline{k}_{0m} = \frac{\underline{Z}_{0m}}{\underline{Z}_{1LA}},$ $\underline{a} = \exp(j2\pi/3), \quad j = \sqrt{-1}$		

10

TABLE 2. Alternative sets of the weighting coefficients for determining a voltage drop across the fault path resistance

Fault type	Set I (recommended)			Set II			Set III		
	$\underline{a}_{F1}$	$\underline{a}_{F2}$	$\underline{a}_{F0}$	$\underline{a}_{F1}$	$\underline{a}_{F2}$	$\underline{a}_{F0}$	$\underline{a}_{F1}$	$\underline{a}_{F2}$	$\underline{a}_{F0}$
a-g	0	3	0	3	0	0	1,5	1,5	0
b-g	0	$3\underline{a}$	0	$3\underline{a}^2$	0	0	$1,5\underline{a}^2$	$1,5\underline{a}$	0
c-g	0	$3\underline{a}^2$	0	$3\underline{a}$	0	0	$1,5\underline{a}$	$1,5\underline{a}^2$	0
a-b	0	$1-\underline{a}$	0	$1-\underline{a}^2$	0	0	$0,5(1-\underline{a}^2)$	$0,5(1-\underline{a})$	0
b-c	0	$\underline{a}-\underline{a}^2$	0	$\underline{a}^2-\underline{a}$	0	0	$0,5(\underline{a}^2-\underline{a})$	$0,5(\underline{a}-\underline{a}^2)$	0
c-a	0	$\underline{a}^2-1$	0	$\underline{a}-1$	0	0	$0,5(\underline{a}-1)$	$0,5(\underline{a}^2-1)$	0
a-b-g	$1-\underline{a}^2$	$1-\underline{a}$	0	$1-\underline{a}^2$	$1-\underline{a}$	0	$1-\underline{a}^2$	$1-\underline{a}$	0
b-c-g	$\underline{a}^2-\underline{a}$	$\underline{a}-\underline{a}^2$	0	$\underline{a}^2-\underline{a}$	$\underline{a}-\underline{a}^2$	0	$\underline{a}^2-\underline{a}$	$\underline{a}-\underline{a}^2$	0
c-a-g	$\underline{a}-1$	$\underline{a}^2-1$	0	$\underline{a}-1$	$\underline{a}^2-1$	0	$\underline{a}-1$	$\underline{a}^2-1$	0
a-b-c-g (a-b-c)	$1-\underline{a}^2$	0	0	$1-\underline{a}^2$	0	0	$1-\underline{a}^2$	0	0
$\underline{a} = \exp(j2\pi/3), \quad j = \sqrt{-1}$									

TABLE 3. Coefficients for determining a fault current distribution factor (6)

SINGLE LINE (Fig.4)	
$\underline{Z}_{1AB} \neq \infty$	$\underline{K}_1 = -\underline{Z}_{1L}\underline{Z}_{1AB} - (\underline{Z}_{1sA} + \underline{Z}_{1sB})\underline{Z}_{1L}$ $\underline{L}_1 = \underline{Z}_{1L}(\underline{Z}_{1sA} + \underline{Z}_{1sB}) + \underline{Z}_{1AB}(\underline{Z}_{1L} + \underline{Z}_{1sB})$ $\underline{M}_1 = (\underline{Z}_{1sA} + \underline{Z}_{1sB})(\underline{Z}_{1AB} + \underline{Z}_{1L}) + \underline{Z}_{1L}\underline{Z}_{1AB}$
$\underline{Z}_{1AB} \rightarrow \infty$	$\underline{K}_1 = -\underline{Z}_{1L} \quad \underline{L}_1 = \underline{Z}_{1L} + \underline{Z}_{1sB}$ $\underline{M}_1 = \underline{Z}_{1sA} + \underline{Z}_{1sB} + \underline{Z}_{1L}$

PARALLEL LINES (Fig.5)	
	$\underline{K}_1 = -\underline{Z}_{1LA}(\underline{Z}_{1sA} + \underline{Z}_{1sB} + \underline{Z}_{1LB\&AB})$ $\underline{L}_1 = \underline{Z}_{1LA}(\underline{Z}_{1sA} + \underline{Z}_{1sB} + \underline{Z}_{1LB\&AB}) + \underline{Z}_{1LB\&AB}\underline{Z}_{1sB1}$ $\underline{M}_1 = \underline{Z}_{1LA}\underline{Z}_{1LB\&AB} + \underline{Z}_{1LA}(\underline{Z}_{1sA} + \underline{Z}_{1sB}) + \underline{Z}_{1LB\&AB}(\underline{Z}_{1sA} + \underline{Z}_{1sB})$ where: $\underline{Z}_{1LB\&AB} = \frac{\underline{Z}_{1LB}\underline{Z}_{1AB}}{\underline{Z}_{1LB} + \underline{Z}_{1AB}}$

Table 4 The recommended SET of the coefficients  $\underline{b}_{F1}, \underline{b}_{F2}$  in relation to (26)

Fault	$\underline{b}_{F1}$	$\underline{b}_{F2}$
a-g	0	1
b-g	0	$\underline{a}^2$
c-g	0	$\underline{a}$
$\underline{a} = \exp(j2\pi/3), \quad j = \sqrt{-1}$		

Table 5 The recommended SET of the coefficients  $\underline{a}_{F1}, \underline{a}_{F2}, \underline{a}_{F0}$  in relation to (26)

FAULT	$\underline{a}_{F1}$	$\underline{a}_{F2}$	$\underline{a}_{F0}$
a-g	0	3	0
b-g	0	$3\underline{a}$	0
c-g	0	$3\underline{a}^2$	0
$\underline{a} = \exp(j2\pi/3), \quad j = \sqrt{-1}$			

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## APPENDICES

A1. DERIVATION OF THE COEFFICIENTS  $a_1$ ,  $a_2$ ,  $a_0$  (TABLE 1)

Single phase-to-ground fault: a-g fault

$$\underline{V}_{AA-p} = \underline{V}_{AA-a} = \underline{V}_{AA1} + \underline{V}_{AA2} + \underline{V}_{AA0} = a_1 \underline{V}_{AA1} + a_2 \underline{V}_{AA2} + a_0 \underline{V}_{AA0}$$

$$\begin{aligned}
 I_{AA\_p} &= I_{AA\_a} + k_0 I_{AA0} + k_{0m} I_{AB0} = I_{AA1} + I_{AA2} + I_{AA0} + \frac{Z_{0LA} - Z_{1LA}}{Z_{1LA}} I_{AA0} + \frac{Z_{0m}}{Z_{1LA}} I_{AB0} = \\
 &= I_{AA1} + I_{AA2} + \frac{Z_{0LA}}{Z_{1LA}} I_{AA0} + \frac{Z_{0m}}{Z_{1LA}} I_{AB0} = a_1 I_{AA1} + a_2 I_{AA2} + a_0 \frac{Z_{0LA}}{Z_{1LA}} I_{AA0} + a_{0m} \frac{Z_{0m}}{Z_{1LA}} I_{AB0}
 \end{aligned}$$

Thus:  $a_1 = a_2 = a_0 = 1$

- 5 Inter-phase faults:  $a-b$ ,  $a-b-g$ ,  $a-b-c$ ,  $a-b-c-g$  faults

$$\begin{aligned}
 V_{AA\_p} &= V_{AA\_a} - V_{AA\_b} = (V_{AA1} + V_{AA2} + V_{AA0}) - (a^2 V_{AA1} + a V_{AA2} + V_{AA0}) = \\
 &= (1 - a^2) V_{AA1} + (1 - a) V_{AA2} = a_1 V_{AA1} + a_2 V_{AA2} + a_0 V_{AA0}
 \end{aligned}$$

$$\begin{aligned}
 I_{AA\_p} &= I_{AA\_a} - I_{AA\_b} = (I_{AA1} + I_{AA2} + I_{AA0}) - (a^2 I_{AA1} + a I_{AA2} + I_{AA0}) = \\
 &= (1 - a^2) I_{AA1} + (1 - a) I_{AA2} = a_1 I_{AA1} + a_2 I_{AA2} + a_0 \frac{Z_{L0}}{Z_{L1}} I_{AA0}
 \end{aligned}$$

Thus:  $a_1 = 1 - a^2$ ,  $a_2 = 1 - a$ ,  $a_0 = 0$

- 10 A2. Derivation of coefficients  $a_{F1}$ ,  $a_{F2}$ ,  $a_{F0}$  (TABLE 2)

TABLE 2 contains three alternative sets (Set I, Set II, Set III) of the weighting coefficients, which are used for determining a voltage drop across a fault path. The coefficients are calculated from the boundary conditions -  
 15 relevant for a particular fault type. This is distinctive that in all the sets the zero sequence is omitted ( $a_{F0} = 0$ ). This is advantage since the zero sequence impedance of a line is considered as the uncertain parameter. By setting  $a_{F0} = 0$  we limit adverse influence of the uncertainty with respect to the  
 20 zero sequence impedance data upon the fault location accuracy. To be precise one has to note that this limitation is of course partial since it is related only to determining the voltage drop across a fault path. In contrast, while determining the voltage drop across a faulted line segment the  
 25 zero sequence impedance of the line is used.

$a-g$  fault, Figure 12:

Taking into account that in the healthy phases:  $I_{F-b} = I_{F-c} = 0$

what gives:  $I_{F1} = \frac{1}{3}(I_{F-a} + aI_{F-b} + a^2I_{F-c}) = \frac{1}{3}(I_{F-a} + a0 + a^20) = \frac{1}{3}I_{F-a}$

$$I_{F2} = \frac{1}{3}(I_{F-a} + a^2I_{F-b} + aI_{F-c}) = \frac{1}{3}(I_{F-a} + a^20 + a0) = \frac{1}{3}I_{F-a}$$

$$I_{F0} = \frac{1}{3}(I_{F-a} + I_{F-b} + I_{F-c}) = \frac{1}{3}(I_{F-a} + 0 + 0) = \frac{1}{3}I_{F-a}$$

5 The sequence components are related:  $I_{F1} = I_{F2} = I_{F0}$  and

finally:

$I_F = I_{F-a} = 3I_{F2}$ , thus:  $a_{F1} = 0$ ,  $a_{F2} = 3$ ,  $a_{F0} = 0$  (as in the Set I from Table 2)

or

10  $I_F = I_{F-a} = 3I_{F1}$ , thus:  $a_{F1} = 3$ ,  $a_{F2} = 0$ ,  $a_{F0} = 0$  (as in the Set II from Table 2)

or

$I_F = I_{F-a} = 1,5I_{F1} + 1,5I_{F2}$ , thus:  $a_{F1} = 1,5$ ,  $a_{F2} = 1,5$ ,  $a_{F0} = 0$  (as in the Set III from Table 2)

15

a-b fault Figure 13a, 13b:

The fault current can be expressed as:  $I_F = I_{F-a}$  or as:

$$I_F = \frac{1}{2}(I_{F-a} - I_{F-b})$$

Taking into account that in the healthy phase:  $I_{F-c} = 0$  and

20 for the faulted phases:  $I_{F-b} = -I_{F-a}$ , what gives:

$$I_{F1} = \frac{1}{3}(I_{F-a} + aI_{F-b} + a^2I_{F-c}) = \frac{1}{3}(I_{F-a} + a(-I_{F-a}) + a^20) = \frac{1}{3}(1-a)I_{F-a}$$

$$I_{F2} = \frac{1}{3}(I_{F-a} + a^2I_{F-b} + aI_{F-c}) = \frac{1}{3}(I_{F-a} + a^2(-I_{F-a}) + a0) = \frac{1}{3}(1-a^2)I_{F-a}$$

$$I_{F0} = \frac{1}{3}(I_{F-a} + I_{F-b} + I_{F-c}) = \frac{1}{3}(I_{F-a} + (-I_{F-a}) + 0) = 0$$

The relation between  $I_{F1}$  and  $I_{F2}$  is thus:

25 
$$\frac{I_{F1}}{I_{F2}} = \frac{\frac{1}{3}(1-a)I_{F-a}}{\frac{1}{3}(1-a^2)I_{F-a}} = \frac{(1-a)}{(1-a^2)}$$

Finally:

$$I_F = I_{F-a} = \frac{3}{(1-a^2)} I_{F2} = (1-a) I_{F2}$$

thus:  $a_{F1} = 0$ ,  $a_{F2} = 1-a$ ,  $a_{F0} = 0$  (as in the Set I from Table 2)

or

$$5 \quad I_F = I_{F-a} = \frac{3}{(1-a)} I_{F1} = (1-a^2) I_{F1}$$

thus:  $a_{F1} = 1-a^2$ ,  $a_{F2} = 0$ ,  $a_{F0} = 0$  (as in the SET 2 from Table 2)

or

$$I_F = 0,5 I_{F-a} + 0,5 I_{F-a} = \frac{1,5}{(1-a^2)} I_{F2} + \frac{1,5}{(1-a)} I_{F1} = 0,5(1-a) I_{F2} + 0,5(1-a^2) I_{F1}$$

thus:  $a_{F1} = 0,5(1-a^2)$ ,  $a_{F2} = 0,5(1-a)$ ,  $a_{F0} = 0$  (as in the Set III

10 from Table 2)

(a-b-g) fault, Figure 14:

$$I_F = I_{F-a} - I_{F-b} = (I_{F1} + I_{F2} + I_{F0}) - (a^2 I_{F1} + a I_{F2} + I_{F0}) = \\ = (1-a^2) I_{F1} + (1-a) I_{F2}$$

15 Thus:  $a_{F1} = 1-a^2$ ,  $a_{F2} = 1-a$ ,  $a_{F0} = 0$  (as in the Sets I, II, III from Table 2)

(a-b-c) or (a-b-c-g) symmetrical faults, Figure 15a, 15b, 15c:

20 Taking the first two phases (a, b) for composing the voltage drop across a fault path one obtains:

$$I_F = I_{F-a} - I_{F-b} = (I_{F1} + I_{F2} + I_{F0}) - (a^2 I_{F1} + a I_{F2} + I_{F0}) = \\ = (1-a^2) I_{F1} + (1-a) I_{F2}$$

Thus:

$$a_{F1} = 1-a^2, \quad a_{F2} = 1-a, \quad a_{F0} = 0$$

25 Additionally, if a fault is ideally symmetrical the positive sequence is the only component, which is present in the signals. Therefore, we have:



$\underline{a}_{F1} = 1 - \underline{a}^2$ ,  $\underline{a}_{F2} = 0$ ,  $\underline{a}_{F0} = 0$  (as in the Sets I, II, III from Table 2).

A3. Derivation of the complex coefficients in the fault  
5 current distribution factors for the positive (negative)  
sequence (Table 3)

a) Case of the single line with extra link 45 between the  
substations (Figure 4)

10 Let us determine the fault current distribution factor for the  
positive sequence (the fault current distribution factor for  
the negative sequence is the same). The equivalent circuit of  
Figure 4 with indicated flow of currents for the incremental  
positive sequence is presented in Figure 16.

15

Considering the closed mesh containing: the local segment of  
the faulted line, the remote segment of the faulted line and  
the extra link between the substations, one can write down:

$$d\underline{Z}_{1L}\Delta\underline{I}_{A1} + (1-d)\underline{Z}_{1L}(\Delta\underline{I}_{A1} - \underline{I}_{F1}) - \underline{Z}_{1AB}\Delta\underline{I}_{AB1} = 0$$

20 From the above equation the unknown current from the extra  
link between the substation can be determined as:

$$\Delta\underline{I}_{AB1} = \frac{\underline{Z}_{1L}}{\underline{Z}_{1AB}}(\Delta\underline{I}_{A1} - (1-d)\underline{I}_{F1})$$

Considering the closed mesh containing the source impedances  
( $\underline{Z}_{1sA}$ ,  $\underline{Z}_{1sB}$ ) and the extra link ( $\underline{Z}_{1AB}$ ) one can write down:

25  $\underline{Z}_{1sA}(\Delta\underline{I}_{A1} + \Delta\underline{I}_{AB1}) + \underline{Z}_{1AB}\Delta\underline{I}_{AB1} + \underline{Z}_{1sB}(\Delta\underline{I}_{A1} + \Delta\underline{I}_{AB1} - \underline{I}_{F1}) = 0$

Substituting the previously determined unknown current from  
the extra link into the above equation one obtains:

$$\underline{k}_{F1} = \frac{\Delta \underline{I}_{A1}}{\underline{I}_{F1}} = \frac{\underline{K}_1 d + \underline{L}_1}{\underline{M}_1}$$

where, as in TABLE 3 (Single line,  $\underline{Z}_{1AB} \neq \infty$ ), we have:

$$\underline{K}_1 = -\underline{Z}_{1L} \underline{Z}_{1AB} - (\underline{Z}_{1sA} + \underline{Z}_{1sB}) \underline{Z}_{1L}$$

$$5 \quad \underline{L}_1 = \underline{Z}_{1L} (\underline{Z}_{1sA} + \underline{Z}_{1sB}) + \underline{Z}_{1AB} (\underline{Z}_{1L} + \underline{Z}_{1sB})$$

$$\underline{M}_1 = (\underline{Z}_{1sA} + \underline{Z}_{1sB}) (\underline{Z}_{1AB} + \underline{Z}_{1L}) + \underline{Z}_{1L} \underline{Z}_{1AB}$$

If there is no extra link between the substations ( $\underline{Z}_{1AB} \rightarrow \infty$ ) one has to consider the closed mesh containing the source impedances ( $\underline{Z}_{1sA}$ ,  $\underline{Z}_{1sB}$ ) and both the segments of the faulted

10 line [ $d \underline{Z}_{1L}$  and  $(1-d) \underline{Z}_{1L}$ ]. For this mesh one can write:

$$(\underline{Z}_{1sA} + d \underline{Z}_{1L}) \Delta \underline{I}_{A1} + [\underline{Z}_{1sB} + (1-d) \underline{Z}_{1L}] (\Delta \underline{I}_{A1} - \underline{I}_{F1}) = 0$$

After rearrangements one obtains:

$$\underline{k}_{F1} = \frac{\Delta \underline{I}_{A1}}{\underline{I}_{F1}} = \frac{\underline{K}_1 d + \underline{L}_1}{\underline{M}_1}$$

where, as in TABLE 3 (Single line,  $\underline{Z}_{1AB} \rightarrow \infty$ ), we have:

$$15 \quad \underline{K}_1 = -\underline{Z}_{1L}$$

$$\underline{L}_1 = \underline{Z}_{1L} + \underline{Z}_{1sB}$$

$$\underline{M}_1 = \underline{Z}_{1sA} + \underline{Z}_{1sB} + \underline{Z}_{1L}$$

20 b) Case of the parallel lines with extra link between the substation (Figure 5)

Let us determine the fault current distribution factor for the positive sequence (the fault current distribution factor for the negative sequence is the same). The equivalent circuit of parallel lines from Fig.5 with indicated flow of currents for  
25 the incremental positive sequence is presented in Figure 17.

The healthy parallel line (LB) and the extra link 55 (AB), which are in parallel connection, have been substituted by the equivalent branch with the equivalent impedance:

$$\underline{Z}_{LB\&AB} = \frac{\underline{Z}_{LB}\underline{Z}_{AB}}{\underline{Z}_{LB} + \underline{Z}_{AB}}.$$

- 5 Considering the closed mesh denoted by (AA, F, BA, BB, AB) one can write:

$$d\underline{Z}_{LA}\Delta I_{AA1} + (1-d)\underline{Z}_{LA}(\Delta I_{AA1} - I_{F1}) - \underline{Z}_{LB\&AB}\Delta I_{LB\&AB1} = 0$$

From the above equation the unknown current from the equivalent branch can be determined as:

$$10 \quad \Delta I_{LB\&AB1} = \frac{\underline{Z}_{LA}}{\underline{Z}_{LB\&AB}}(\Delta I_{AA1} - (1-d)I_{F1})$$

Considering the closed mesh containing the source impedances ( $\underline{Z}_{1sA}$ ,  $\underline{Z}_{1sB}$ ) and the equivalent branch ( $\underline{Z}_{LB\&AB}$ ) one can write down:

$$\underline{Z}_{1sA}(\Delta I_{AA1} + \Delta I_{LB\&AB1}) + \underline{Z}_{LB\&AB}\Delta I_{LB\&AB1} + \underline{Z}_{1sB}(\Delta I_{AA1} + \Delta I_{LB\&AB1} - I_{F1}) = 0$$

- 15 Substituting the previously determined unknown current from the healthy line into the above equation one obtains:

$$\underline{k}_{F1} = \frac{\Delta I_{AA1}}{I_{F1}} = \frac{\underline{K}_1 d + \underline{L}_1}{\underline{M}_1}$$

where, as in TABLE 3 (Parallel lines), we have:

$$\underline{K}_1 = -\underline{Z}_{LA}(\underline{Z}_{1sA} + \underline{Z}_{1sB} + \underline{Z}_{LB\&AB}),$$

$$20 \quad \underline{L}_1 = \underline{Z}_{LA}(\underline{Z}_{1sA} + \underline{Z}_{1sB} + \underline{Z}_{LB\&AB}) + \underline{Z}_{LB\&AB}\underline{Z}_{1sB1}$$

$$\underline{M}_1 = \underline{Z}_{LA}\underline{Z}_{LB\&AB} + \underline{Z}_{LA}(\underline{Z}_{1sA} + \underline{Z}_{1sB}) + \underline{Z}_{LB\&AB}(\underline{Z}_{1sA} + \underline{Z}_{1sB})$$

$$\text{where: } \underline{Z}_{LB\&AB} = \frac{\underline{Z}_{LB}\underline{Z}_{AB}}{\underline{Z}_{LB} + \underline{Z}_{AB}}.$$

In case if the extra link between the substations ( $\underline{Z}_{AB}$ ) is not present one has to substitute:  $\underline{Z}_{LB\&AB} = \underline{Z}_{LB}$ .

## CLAIMS

1. A method to locate a fault from one end of a section of a power line (A-B) by means of measurements of current, voltage and angles between the phases at a first (A) end of said section, and that upon detection of a fault condition between

5 said first end and a second end of said power line, **characterised by**

-calculating symmetrical components of currents for said current and voltage measure at said first end,

10 -calculating a distance (d) from said first end (2) to the fault (F) the distance (d) to the fault using a quadratic equation of the form:

$$B_2 d^2 + B_1 d + B_0 = 0$$

where:

15  $B_2 = A_{2\_Re} A_{00\_Im} - A_{2\_Im} A_{00\_Re}$

$$B_1 = A_{1\_Re} A_{00\_Im} - A_{1\_Im} A_{00\_Re}$$

$$B_0 = A_{0\_Re} A_{00\_Im} - A_{0\_Im} A_{00\_Re}$$

2. A method according to claim 1, **characterised by** calculating

20 the distance (d) to the fault using an equation of the form:

$$K_1 Z_{1L} d^2 + (L_1 Z_{1L} - K_1 Z_{AA\_P}) d - L_1 Z_{AA\_P} + R_F M_1 \frac{(a_{F1} \Delta I_{AA1} + a_{F2} I_{AA2})}{I_{AA\_P}} = 0 \quad (8)$$

where:

$$Z_{AA\_P} = \frac{V_{AA\_P}}{I_{AA\_P}} - \text{calculated fault loop impedance.}$$

25 3. A method according to any of claims 1 or 2, **characterised** by calculating the distance (d) to the fault using an equation of the form:

$$\underline{A}_2 d^2 + \underline{A}_1 d + \underline{A}_0 + \underline{A}_{00} R_F = 0$$

where:

30  $\underline{A}_2 = A_{2\_Re} + j A_{2\_Im} = K_1 Z_{1LA}$

$$\underline{A}_1 = A_{1\_Re} + j A_{1\_Im} = L_1 Z_{1LA} - K_1 Z_{AA\_P}$$

$$\underline{A}_0 = A_{0\_Re} + jA_{0\_Im} = -\underline{L}_1 \underline{Z}_{AA\_p}$$

$$A_{00\_Re} + jA_{00\_Im} = \frac{\underline{M}_1(a_{F1}\underline{I}_{AA1} + a_{F2}\underline{I}_{AA2})}{\underline{I}_{AA\_p}}$$

$$\underline{Z}_{AA\_p} = \frac{\underline{V}_{AA\_p}}{\underline{I}_{AA\_p}} = \text{calculated fault loop impedance}$$

$\underline{K}_1, \underline{L}_1, \underline{M}_1$  = coefficients gathered in TABLE 3.

5

4. A method according to any of claims 1-3, **characterised** by  
 -determining source impedance at said first end as a  
 representative value, and  
 -determining a value for source impedance at said second end  
 as a representative value.

10

5. A method according to any of claims 1-4, **characterised** by  
 calculating a value for impedance of an extra link (45, 55)  
 between the terminals A, B, as having impedance for the  
 positive sequence equal to:

15

$$(\underline{Z}_{1LB\&AB} = \frac{\underline{Z}_{1LB}\underline{Z}_{1AB}}{\underline{Z}_{1LB} + \underline{Z}_{1AB}})$$

where

$\underline{Z}_{1AB}$  = impedance for the positive sequence of the extra link,

$\underline{Z}_{1LA}$  = positive sequence impedance of the healthy line.

20

6. A method according to any of claims 1-5, **characterised** by  
 calculating symmetrical components of currents for said  
 current and voltage measured at said first end by:

-inputting instantaneous phase voltages (30a),

-filtering (33a) the values to determine the phasors, and

-calculating (34a) phasors of symmetrical components of  
 voltages.

7. A method according to any of claims 1-6, **characterised** by  
 calculating symmetrical components of currents for said  
 current and voltage measured at said first end by

30

-inputting instantaneous phase currents and instantaneous zero sequence current from a healthy line(30b),  
 -filtering (33b) the values to determine the phasors, and  
 -calculating (34b) phasors of symmetrical components of  
 5 currents.

8. A method according to any of claims 1-7, **characterised** by determining a compensation for shunt capacitance by means of an equation of the form:

$$10 \quad B_2^{comp-1}(d_{comp-1})^2 + B_1^{comp-1}d_{comp-1} + B_0^{comp-1} = 0 \quad (22)$$

where:

$$B_2^{comp-1} = A_{2\_Re}^{comp-1}A_{00\_Im}^{comp-1} - A_{2\_Im}^{comp-1}A_{00\_Re}^{comp-1}$$

$$B_1^{comp-1} = A_{1\_Re}^{comp-1}A_{00\_Im}^{comp-1} - A_{1\_Im}^{comp-1}A_{00\_Re}^{comp-1}$$

$$B_0^{comp-1} = A_{0\_Re}^{comp-1}A_{00\_Im}^{comp-1} - A_{0\_Im}^{comp-1}A_{00\_Re}^{comp-1}$$

15

9. A method according to claim 8, **characterised** by determining a compensation for shunt capacitance by means of an equation of the form:

$$\underline{A}_2^{comp-1}(d_{comp-1})^2 + \underline{A}_1^{comp-1}d_{comp-1} + \underline{A}_0^{comp-1} + \underline{A}_{00}^{comp-1}R_F = 0 \quad (21a)$$

20 where:

$$\underline{A}_2^{comp-1} = A_{2\_Re}^{comp-1} + jA_{2\_Im}^{comp-1} = \underline{K}_1 \underline{Z}_{1L}^{long}$$

$$\underline{A}_1^{comp-1} = A_{1\_Re}^{comp-1} + jA_{1\_Im}^{comp-1} = \underline{L}_1 \underline{Z}_{1L}^{long} - \underline{K}_1 \underline{Z}_{A-p}^{comp-1}$$

$$\underline{A}_0^{comp-1} = A_{0\_Re}^{comp-1} + jA_{0\_Im}^{comp-1} = -\underline{L}_1 \underline{Z}_{A-p}^{comp-1}$$

$$\underline{A}_{00}^{comp-1} = A_{00\_Re}^{comp-1} + jA_{00\_Im}^{comp-1} = \frac{\underline{M}_1(a_{F1}\underline{A}I_{AA1} + a_{F2}\underline{A}I_{AA2})}{\underline{I}_{A-p}^{comp-1}}$$

$$25 \quad \underline{Z}_{A-p}^{comp-1} = \frac{\underline{V}_{A-p}}{\underline{I}_{A-p}^{comp-1}} - \text{fault loop impedance calculated from:}$$

$\underline{V}_{A-p}$  - original (uncompensated) fault loop voltage,

$\underline{I}_{A-p}^{comp-1} = a_1 \underline{I}_{A1\_comp-1} + a_2 \underline{I}_{A2\_comp-1} + a_0 \underline{I}_{A0\_comp-1}$  - fault loop current composed of the positive (12), negative (16) and zero (17)

sequence currents obtained after deducing the respective capacitive currents from the original currents, and

$\underline{K}_1$ ,  $\underline{L}_1$ ,  $\underline{M}_1$  = coefficients gathered in TABLE 3.

5 10. A method according to any of claims 1-9, characterised by measuring the source impedance  $\underline{Z}_{1sA}$  at said first end A.

11. A method according to any of claims 1-9, characterised by  
-measuring the source impedance  $\underline{Z}_{1sB}$  at said second end B,

10 -sending a communication of the measured value of source impedance  $\underline{Z}_{1sB}$  at said second end B to a fault locator at said first end A.

12. A method according to any of claims 1-11, characterised by

15 -determining the zero sequence current from the healthy line of a section of parallel power lines,  
-calculating a distance to a fault for the parallel line section.

20 13. A method according to claim 12, characterised by determining distance to a single phase to ground fault without measurements from an operating healthy parallel line by means of complex coefficients  $\underline{P}_0$  according to a formula of the form:

$$\underline{P}_0 = \frac{\underline{Z}_{0LB} - \underline{Z}_{0m}}{\underline{Z}_{0LA} - \underline{Z}_{0m}}$$

25 and  $\underline{K}_1$ ,  $\underline{L}_1$ ,  $\underline{M}_1$  according to

$$\underline{K}_1 = -\underline{Z}_{1LA}(\underline{Z}_{1sA} + \underline{Z}_{1sB} + \underline{Z}_{1LB})$$

$$\underline{L}_1 = -\underline{K}_1 + \underline{Z}_{1LB}\underline{Z}_{1sB}$$

$$\underline{M}_1 = \underline{Z}_{1LA}\underline{Z}_{1LB} + \underline{Z}_{1LA}(\underline{Z}_{1sA} + \underline{Z}_{1sB}) + \underline{Z}_{1LB}(\underline{Z}_{1sA} + \underline{Z}_{1sB})$$

30 14. A method according to claim 12, characterised by determining distance to a single phase to ground fault without

measurements from switched off and grounded parallel line by means of complex coefficients  $\underline{P}_0$  according to

$$\underline{P}_0 = -\frac{\underline{Z}_{0LB}}{\underline{Z}_{0m}}$$

and  $\underline{K}_1$ ,  $\underline{L}_1$ ,  $\underline{M}_1$  according to

$$5 \quad \underline{K}_1 = -\underline{Z}_{1LA}$$

$$\underline{L}_1 = \underline{Z}_{1LA} + \underline{Z}_{1sB}$$

$$\underline{M}_1 = \underline{Z}_{1sA} + \underline{Z}_{1sA} + \underline{Z}_{1fA}$$

15. A method according to claim 12, **characterised** by  
 10 determining distance to a single ground fault using a first order formula (27a,b,c) of the form:

$$d = \frac{\text{imag}\{\underline{V}_{AA-p}[3(\underline{I}_{AA0} - \underline{P}_0 \underline{I}_{AB0})]^*\}}{\text{imag}\{(\underline{Z}_{1LA} \underline{I}_{AA-p})[3(\underline{I}_{AA0} - \underline{P}_0 \underline{I}_{AB0})]^*\}}$$

16. A method according to claim 12, **characterised** by  
 15 determining distance to a phase-to-phase ground fault using pre-fault measurements and a first order formula (28a,b,c) of the form:

$$d = \frac{\text{imag}\{\underline{V}_{AA-p}[\underline{W}(\underline{I}_{AA0} - \underline{P}_0 \underline{I}_{AB0})]^*\}}{\text{imag}\{(\underline{Z}_{1LA} \underline{I}_{AA-p})[\underline{W}(\underline{I}_{AA0} - \underline{P}_0 \underline{I}_{AB0})]^*\}}$$

- 20 17. A method according to claim 12, **characterised** by determining distance to a phase-to-phase ground fault avoiding pre-fault measurements and using a first order formula (29a,b,c) of the form:

$$d = \frac{\text{imag}\{(\underline{V}_a + \underline{V}_b)(\underline{I}_{AA0} - \underline{P}_0 \underline{I}_{AB0})^*\}}{\text{imag}\{[\underline{Z}_{1LA}(\underline{I}_a + \underline{I}_b + 2\underline{k}_0 \underline{I}_{AA0} + 2\underline{k}_{0m} \underline{I}_{AB0})(\underline{I}_{AA0} - \underline{P}_0 \underline{I}_{AB0})^*]\}}$$



18. A device for locating a fault from one end of a section of a power line (A-B) having means for receiving and storing measurements of current, voltage and angles between the phases at one first end (A), means for receiving and storing a  
5 detection of a fault condition between said first and second ends (A,B), characterised by:

- means for calculating symmetrical components of currents for said current and voltage measured at said first end,
- means for calculating a distance (d) from said first end (2)  
10 to the fault (F).

19. A device according to claim 18, characterised by comprising:

- means for determining a value for source impedance at said  
15 first end,
- means for determining a value for source impedance at said second end.

20. A device according to any of claims 18-19, characterised by comprising:

- means for receiving a measurement of source impedance at said first end A.

21. A device according to any of claims 18-20, characterised by comprising:

- means for receiving a measurement of source impedance made at said second end B.

22. A device according to any of claims 18-21, characterised by comprising means to receive a measured value (9) for remote source impedance at said second end (B) communicated by means of a communication channel (60).  
30

23. Use of a fault locator device according to any of claims 18-22, by a human operator to supervise a function in an electrical power system.

5 24. Use of a fault locator device according to any of claims 18-22, by means of a process running on one or more computers to supervise and/or control a function in an electrical power system.

10 25. Use of a fault locator device according to any of claims 18-22, to locate a distance to a fault in a power transmission or distribution system.

15 26. Use of a device according to any of claims 18-22, for locating a fault on parallel power lines.

27. A computer program comprising computer code means and/or software code portions for making a computer or processor perform any of the steps of claims 1-17.

20

28. A computer program product according to claim 27 comprised in one or more computer readable media.

25 29. A data communication signal for locating a fault in a section of a power line included in a data transmission comprising a value of a measurement of source impedance made in respect of a remote and second (B) end of said section of a power line.

30 30. A graphic user interface for displaying a location of a fault in a section of a power line wherein a value is displayed for a distance (d) of said fault from a first end (A) of said power line.

31. A graphic user interface according to claim 30, characterised in that the value displayed for the distance (d) is combined with a graphical representation of the relevant power line section or network.

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32. A graphic user interface according to claim 30, characterised in that the value displayed for the distance (d) is arranged to be displayed upon activation of a part of the graphical representation of the relevant power line section or network using a computer mouse or similar computer display selection means.

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## ABSTRACT

A method to locate a fault from one end of a section of a power line (A-B). Measurements of current, voltage and angles between the phases are made at a first A end of a said power line section. Upon detection of a fault condition between said first end and a second end of said power line, distance to the fault is found by calculating symmetrical components of currents for said current and voltage measure at said first end, then calculating a distance  $d$  from said first end to the fault F the distance  $d$  to the fault using a quadratic equation of the form:

$$B_2 d^2 + B_1 d + B_0 = 0$$

where:

$$B_2 = A_{2\_Re} A_{00\_Im} - A_{2\_Im} A_{00\_Re}$$

$$B_1 = A_{1\_Re} A_{00\_Im} - A_{1\_Im} A_{00\_Re}$$

A value for source impedance at the first and/or second end used in the method may be a representative value or a measured value. In other aspects of the invention a fault locator device for carrying out the method and a computer program for carrying out the method are described

(Figure 1)



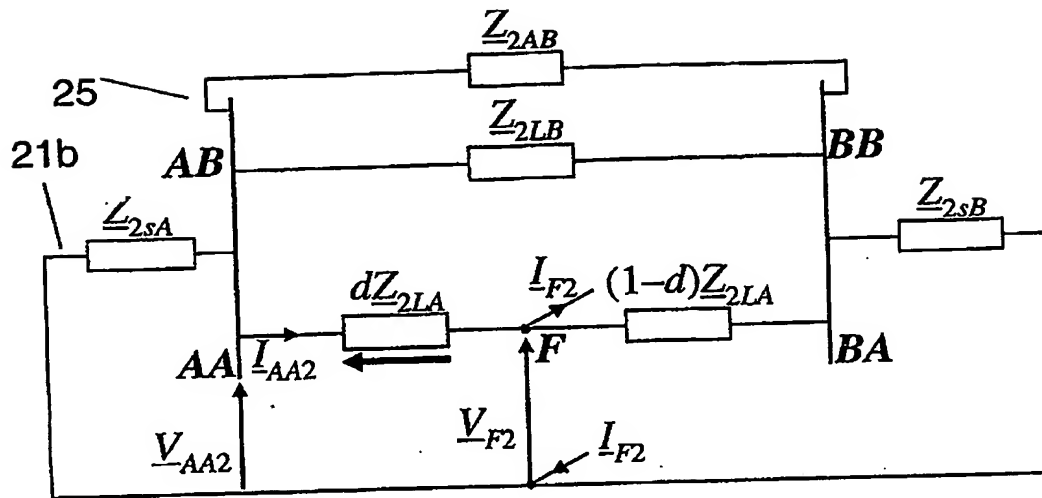


Figure 2b

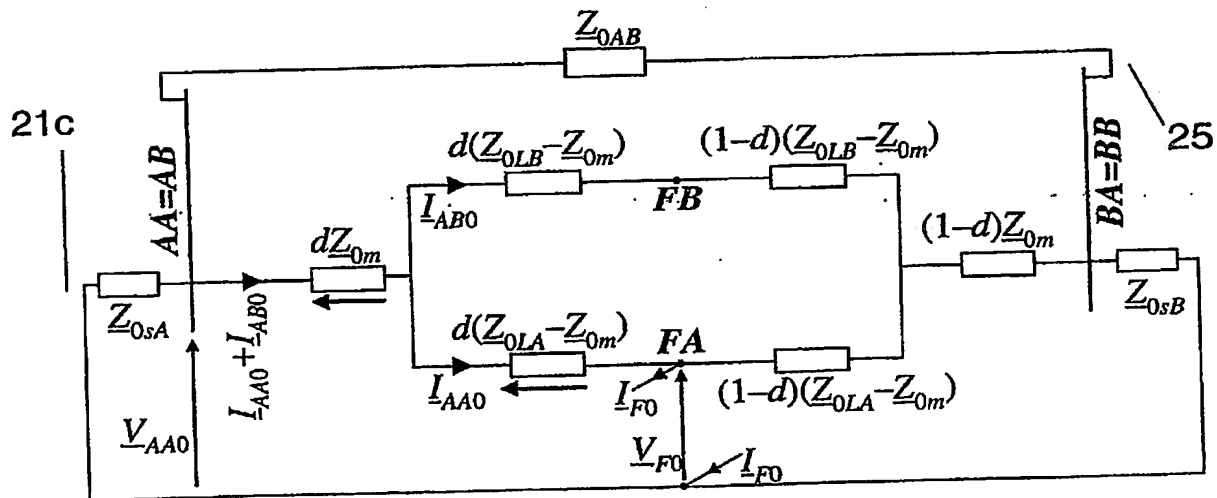


Figure 2c

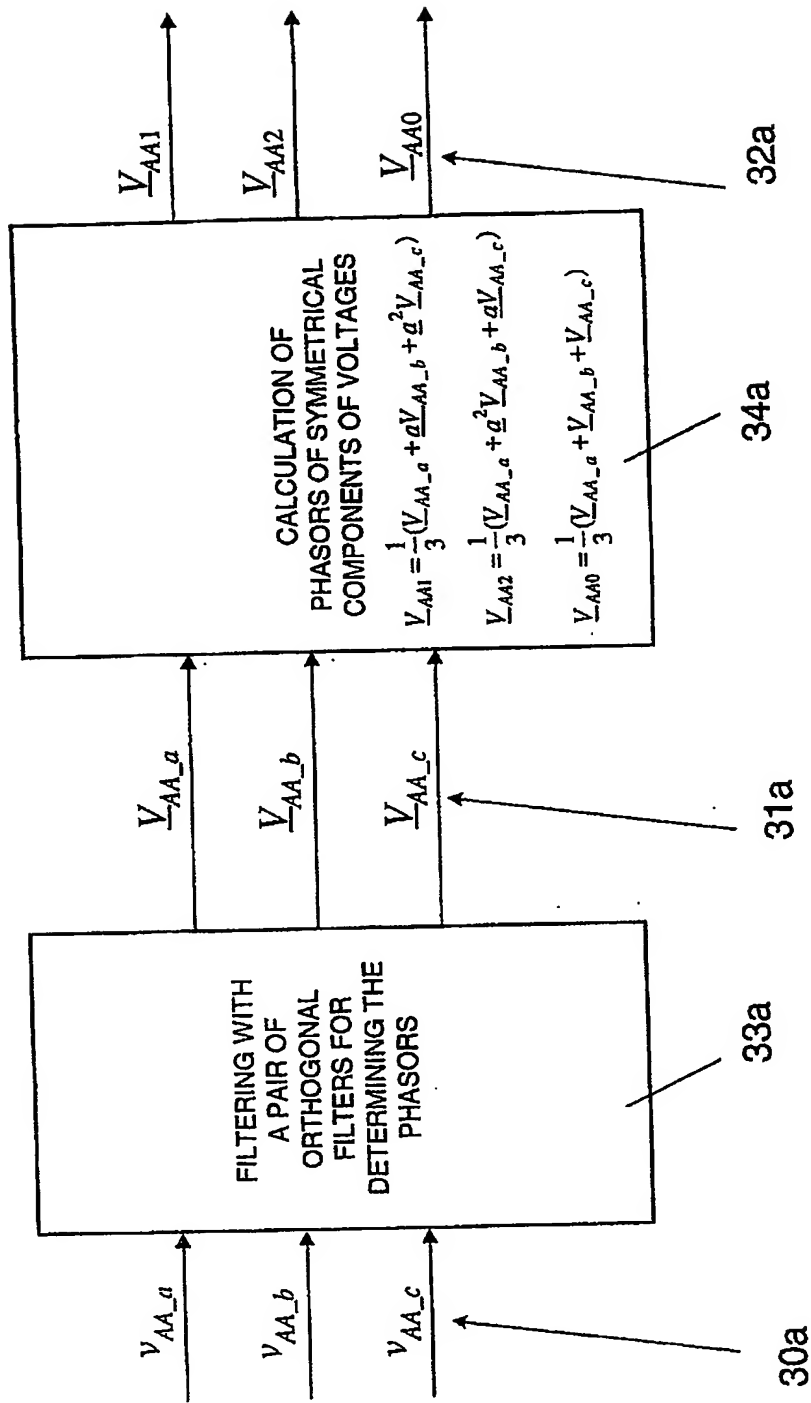


Figure 3a

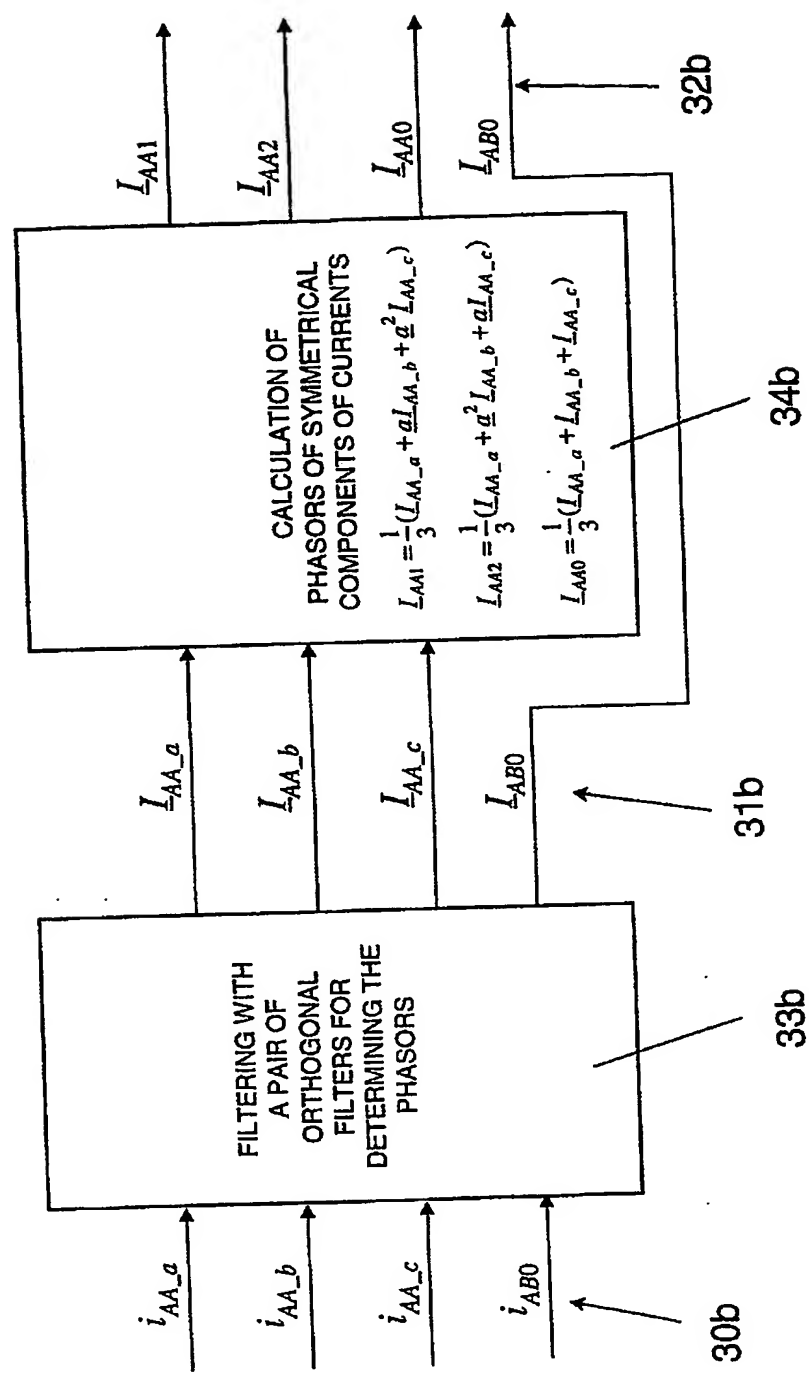


Figure 3b

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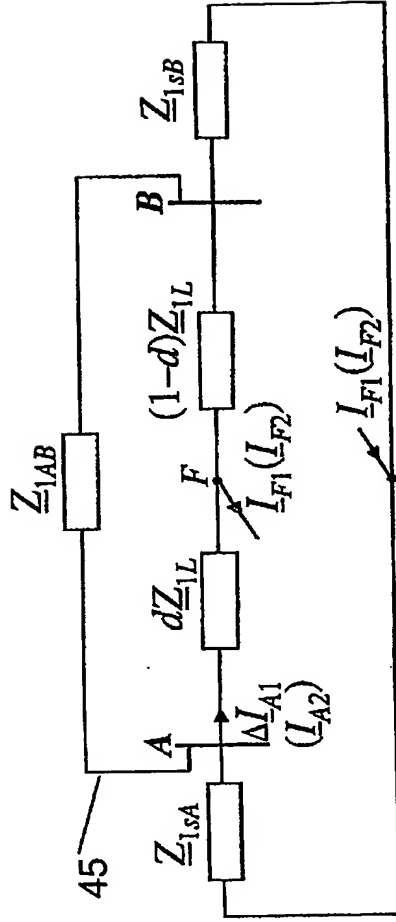


Figure 4

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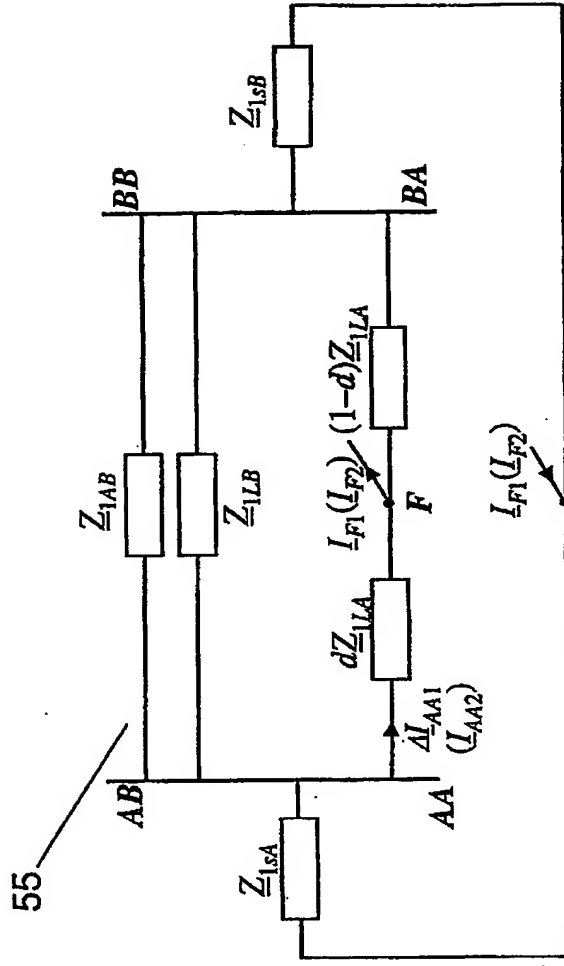


Figure 5

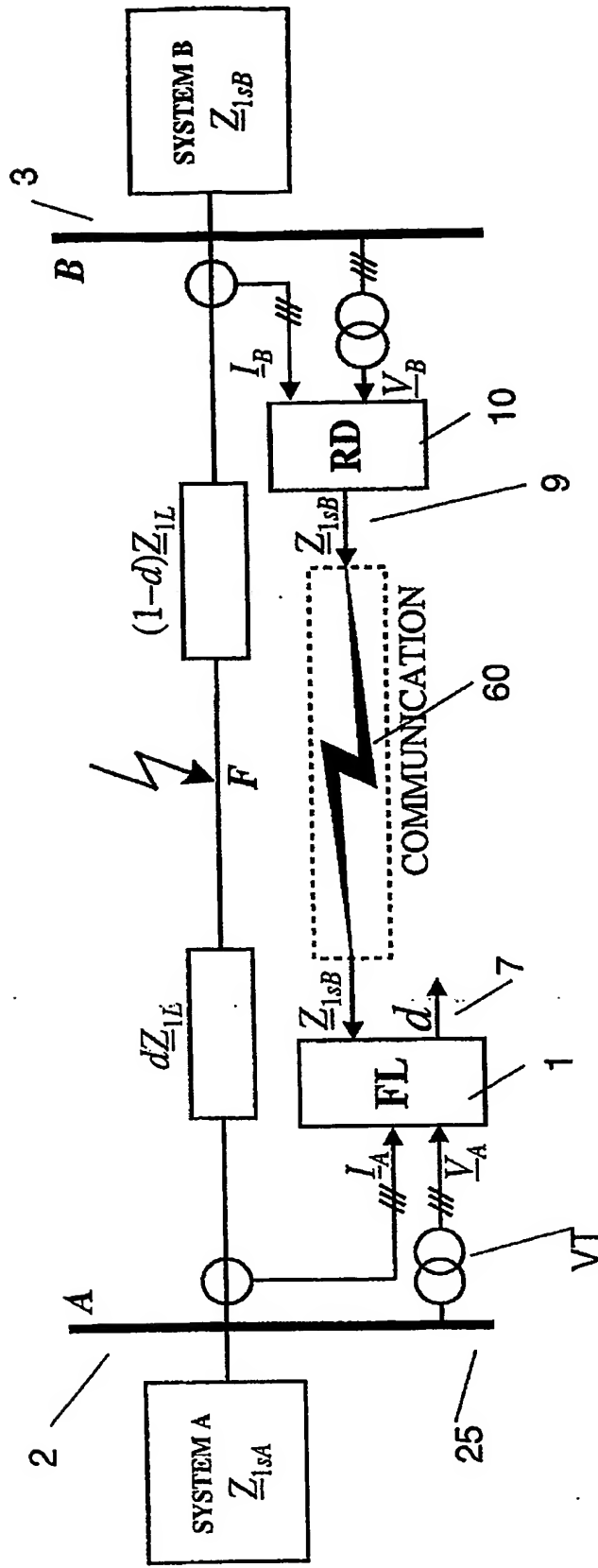


Figure 6

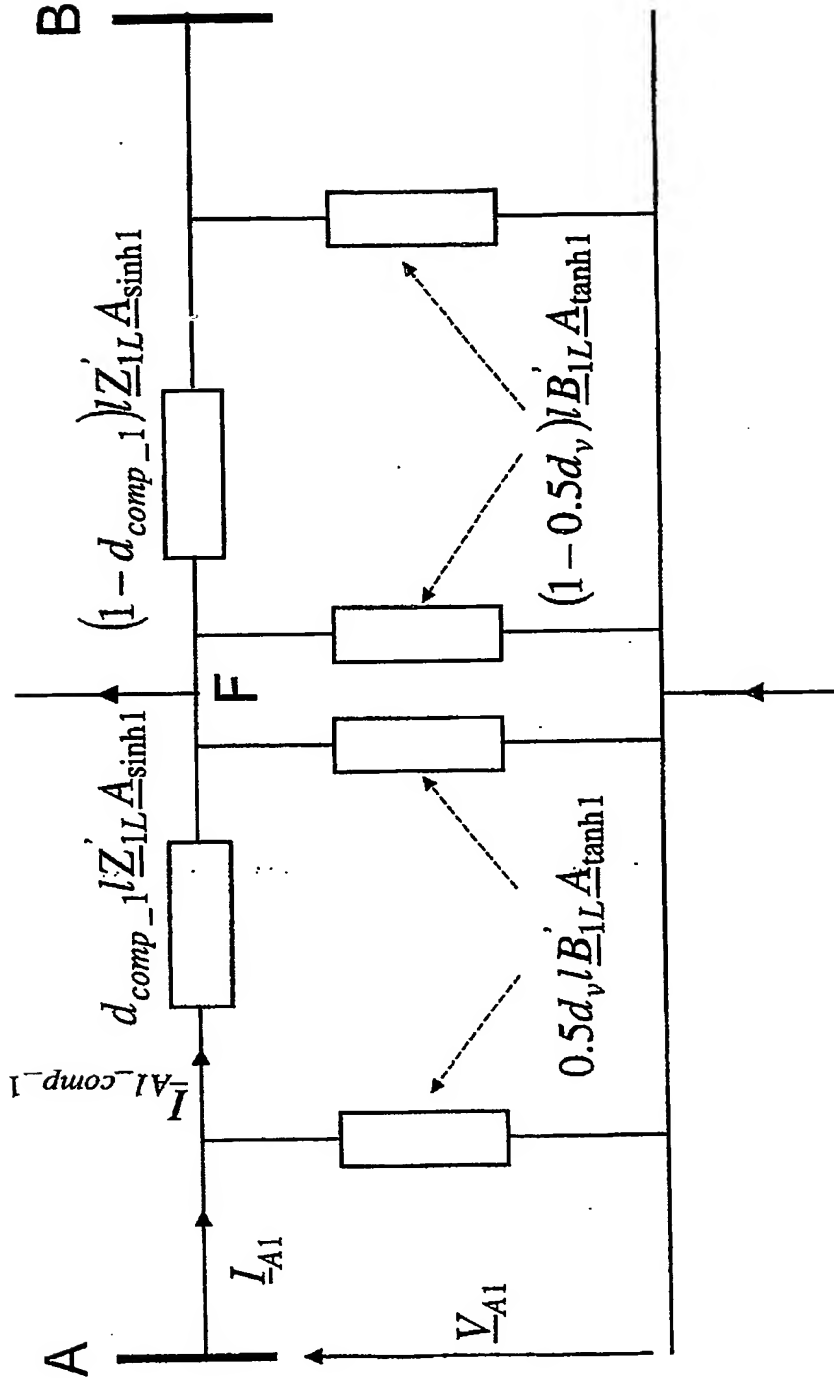


Figure 7

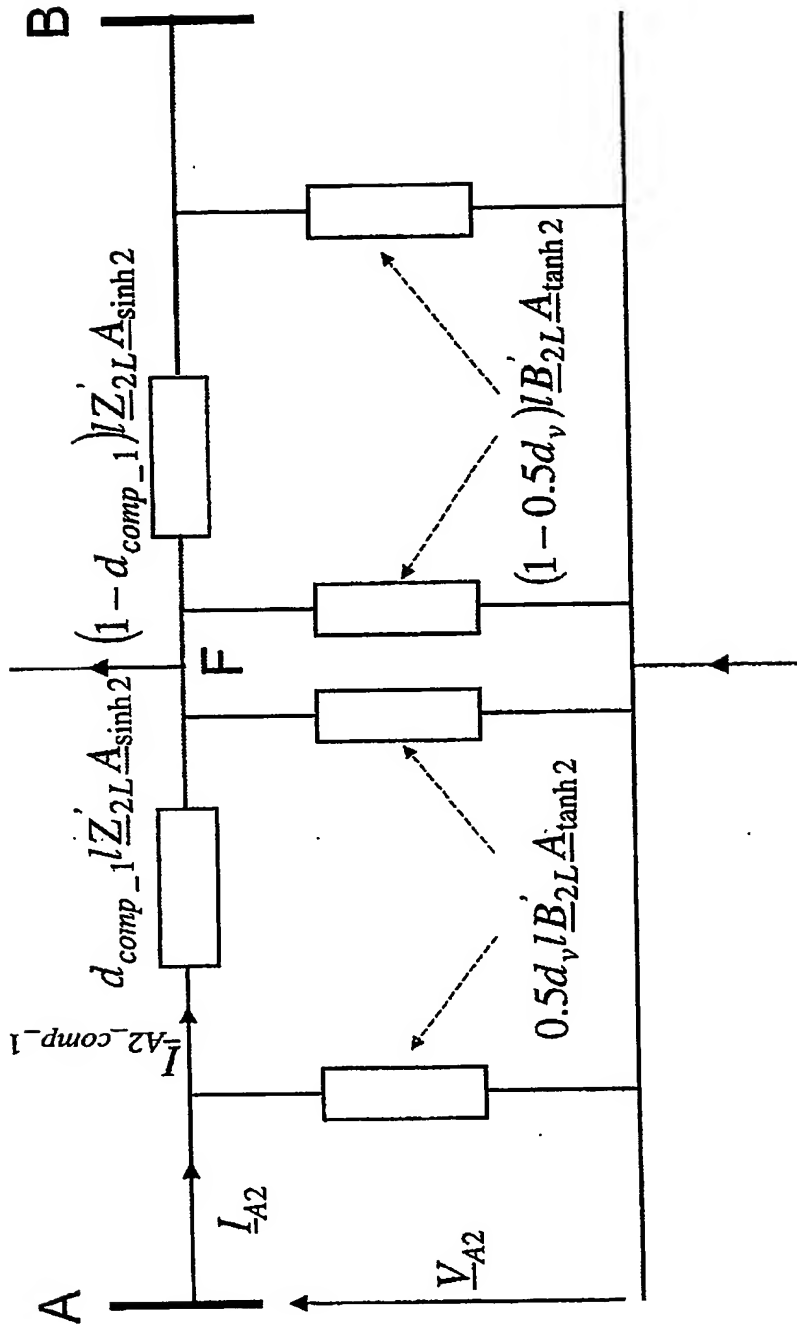


Figure 8

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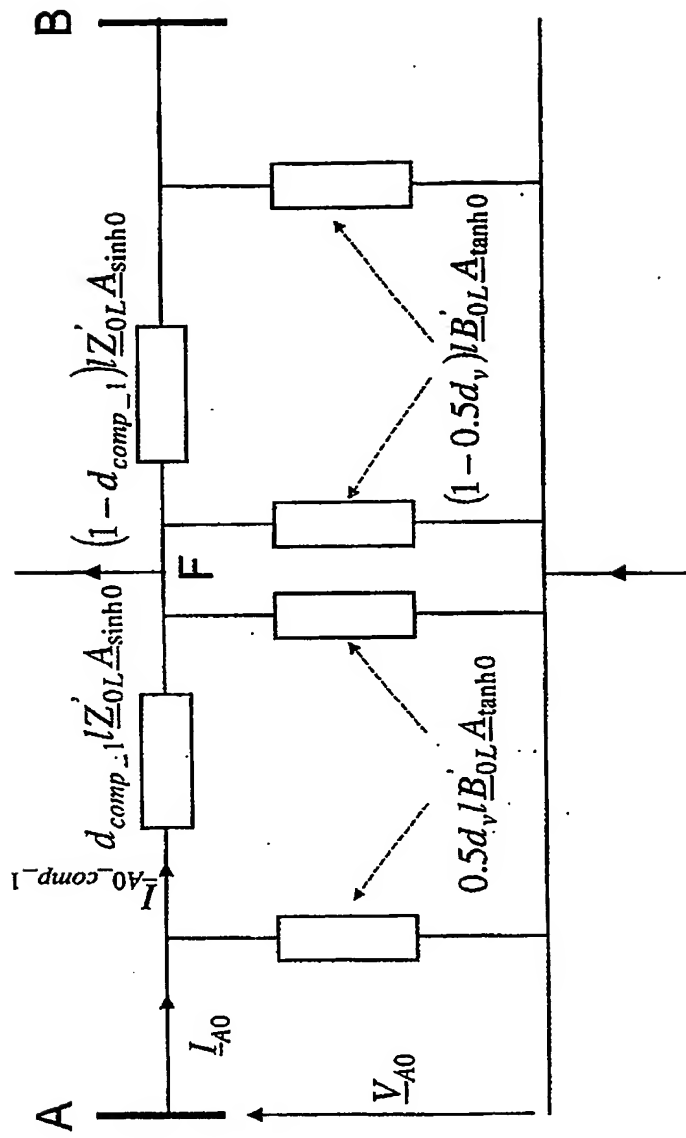


Figure 9

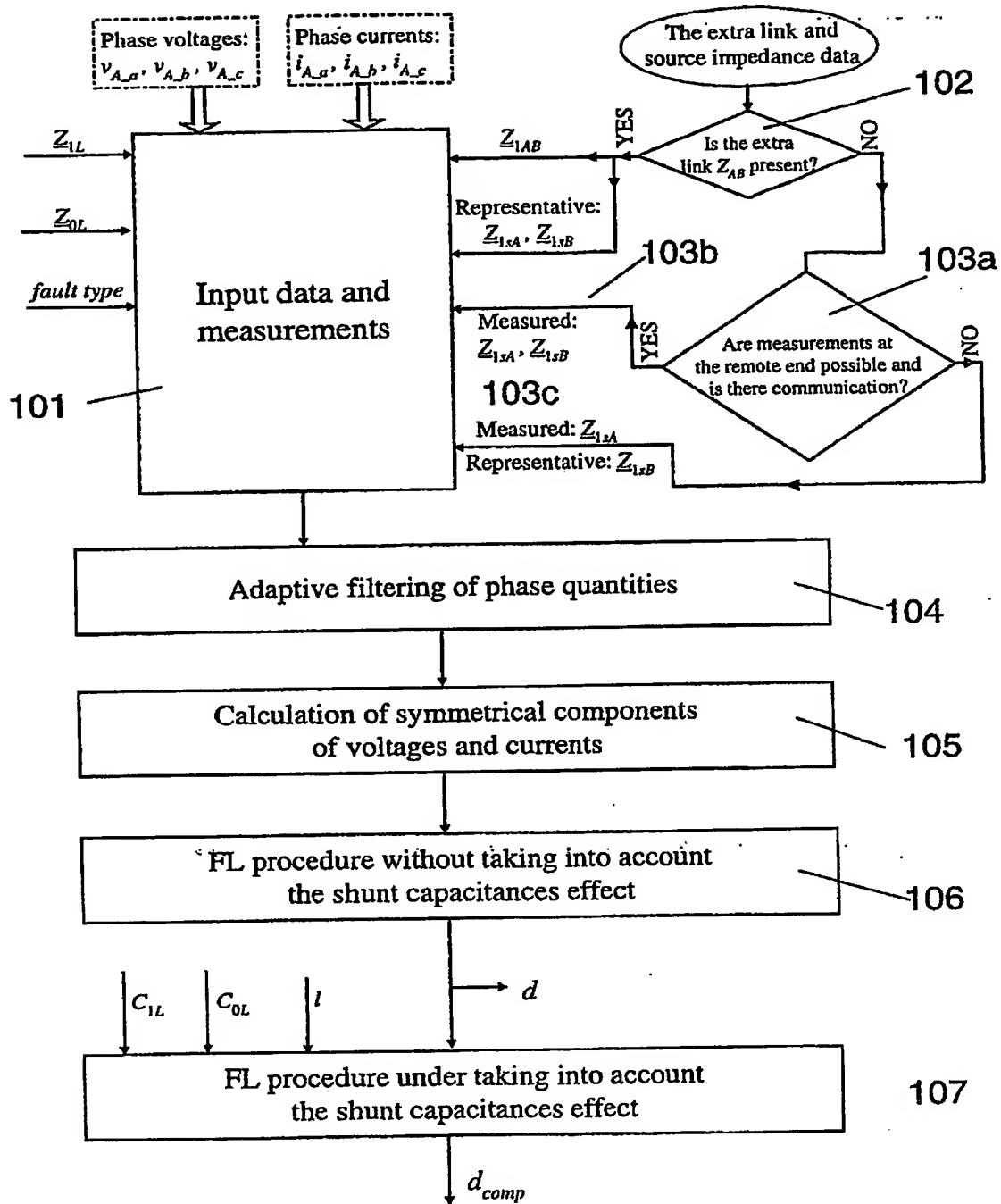


Figure 10

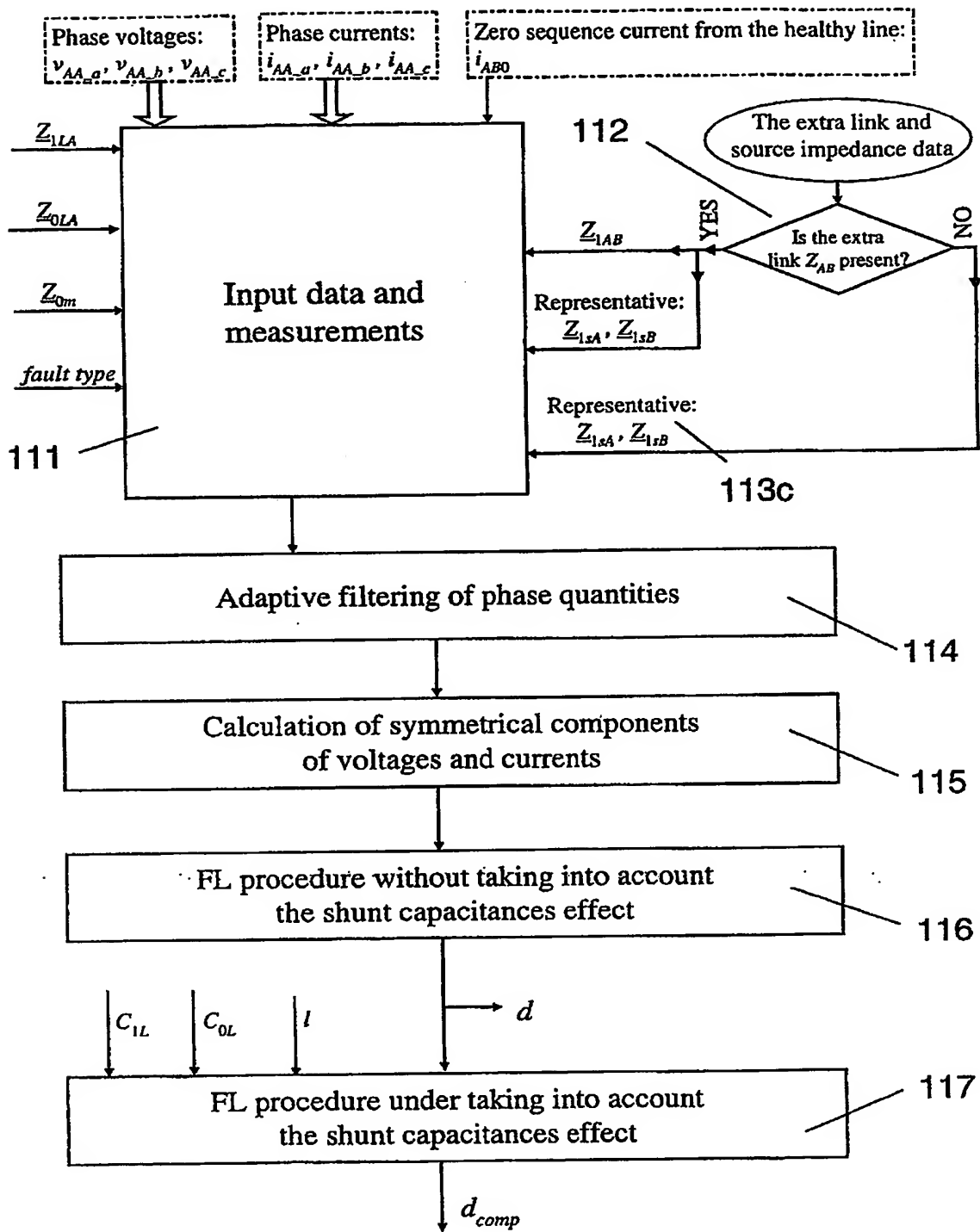


Figure 11



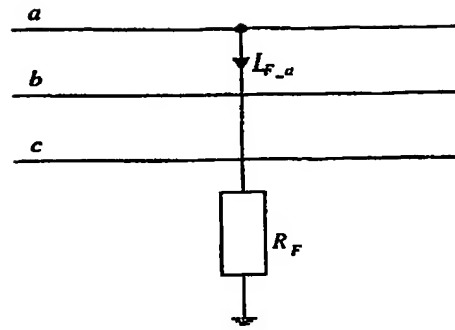


Figure 12

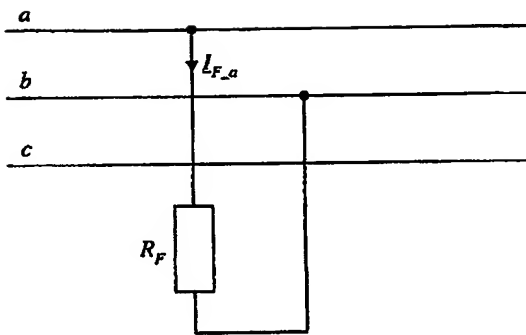


Figure 13a

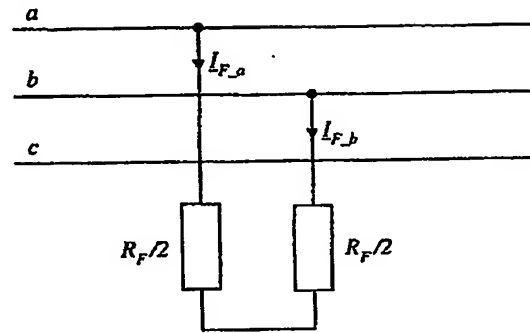


Figure 13b

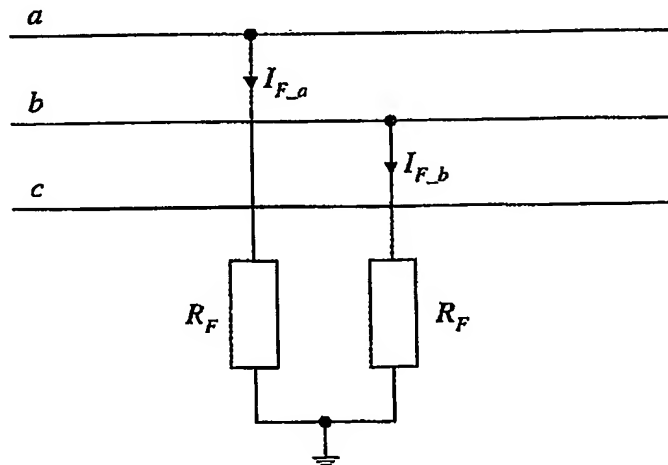


Figure 14

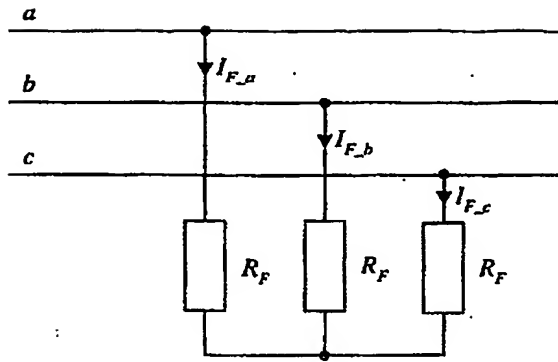


Figure 15a

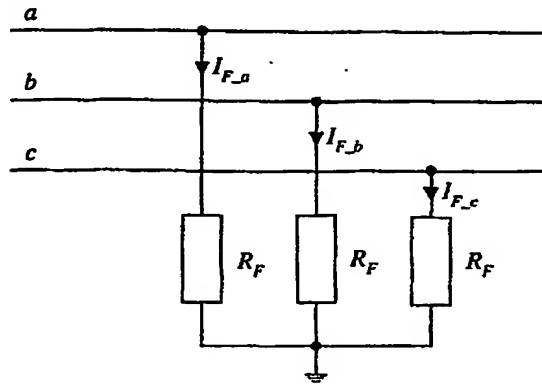


Figure 15b

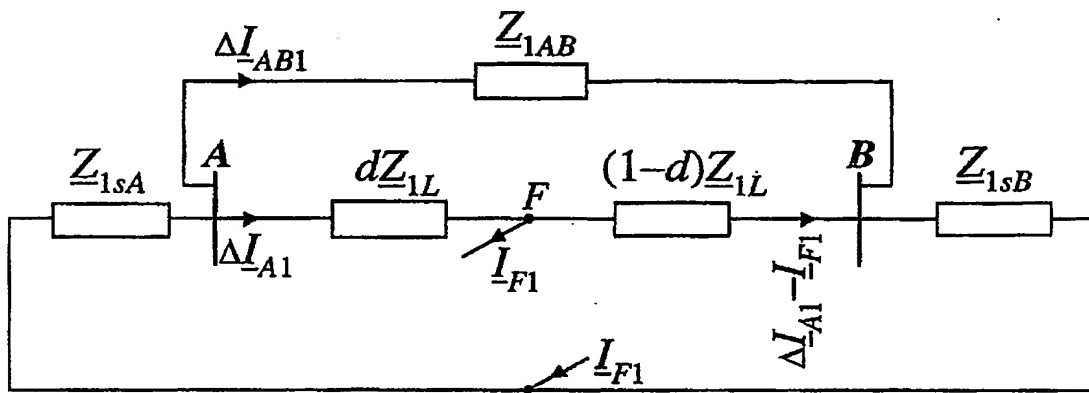


Figure 16

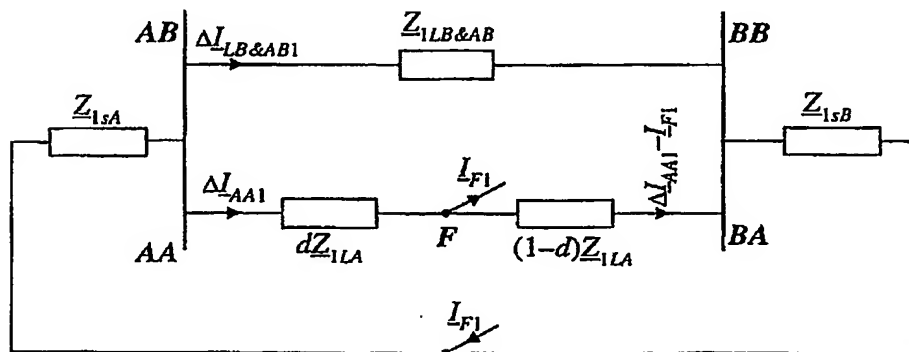


Figure 17

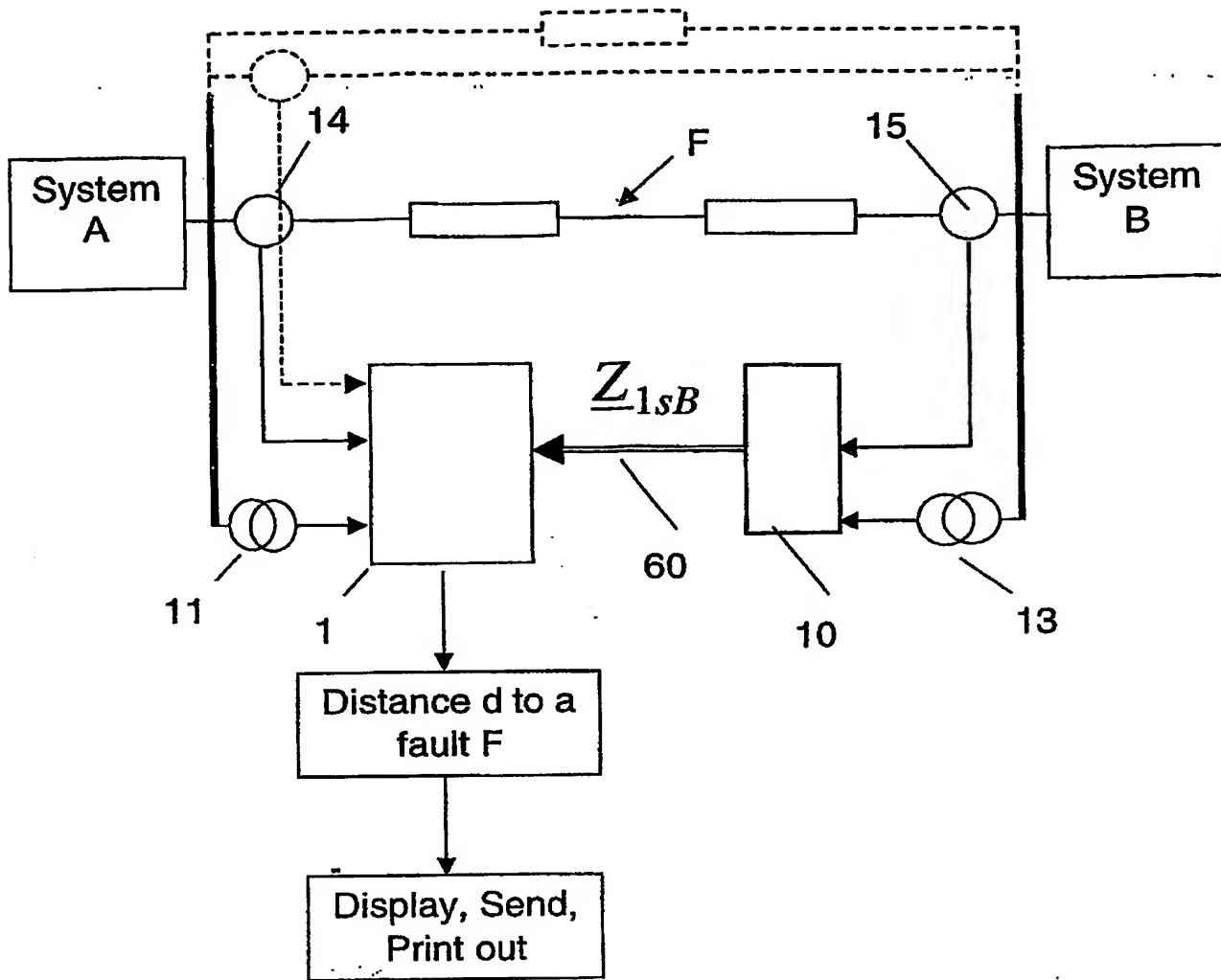


Figure 18

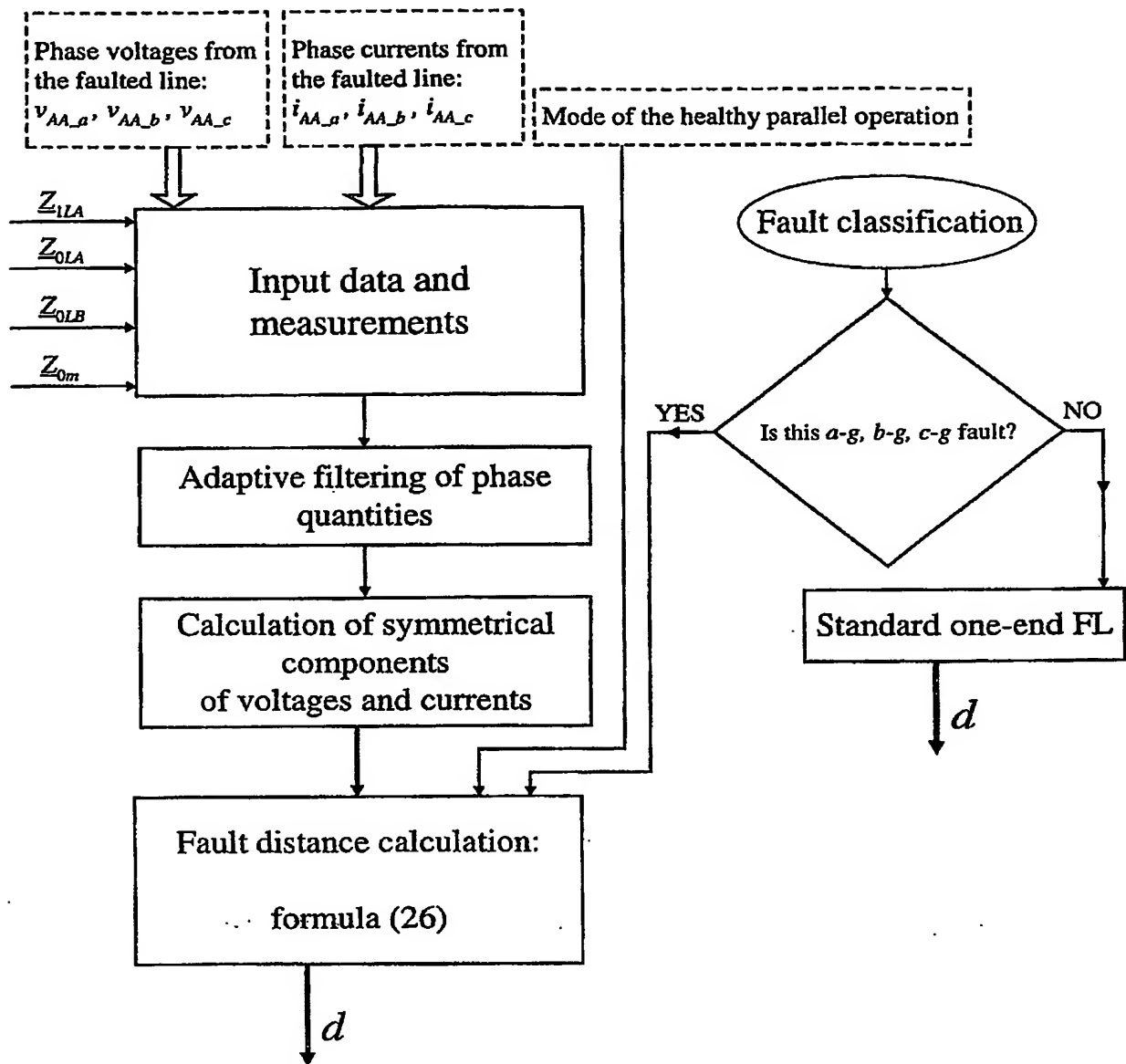


Figure 19

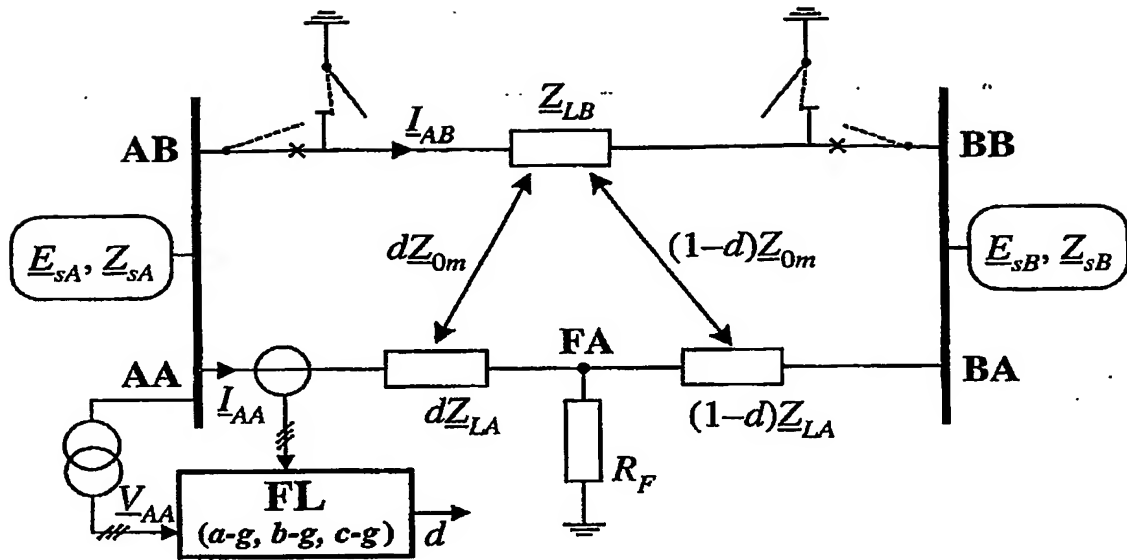


Figure 20

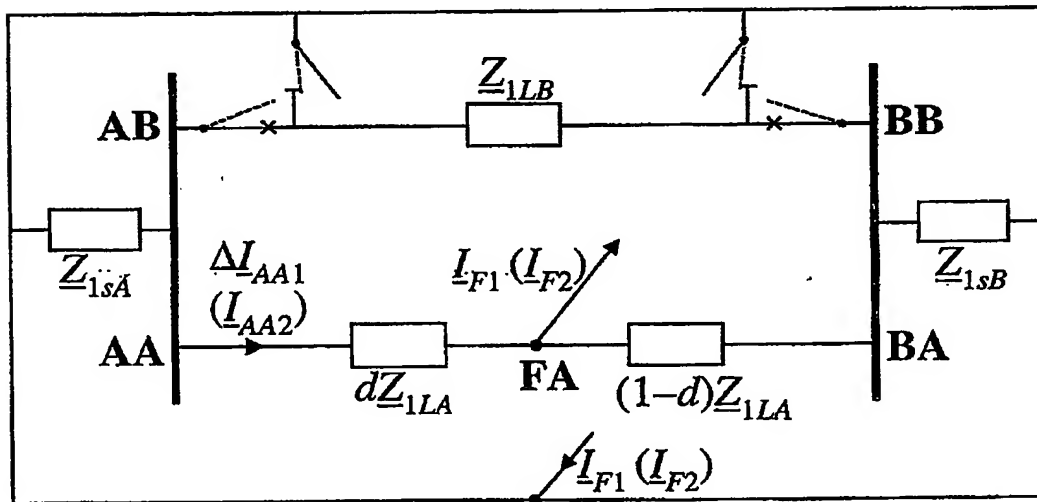


Figure 21a

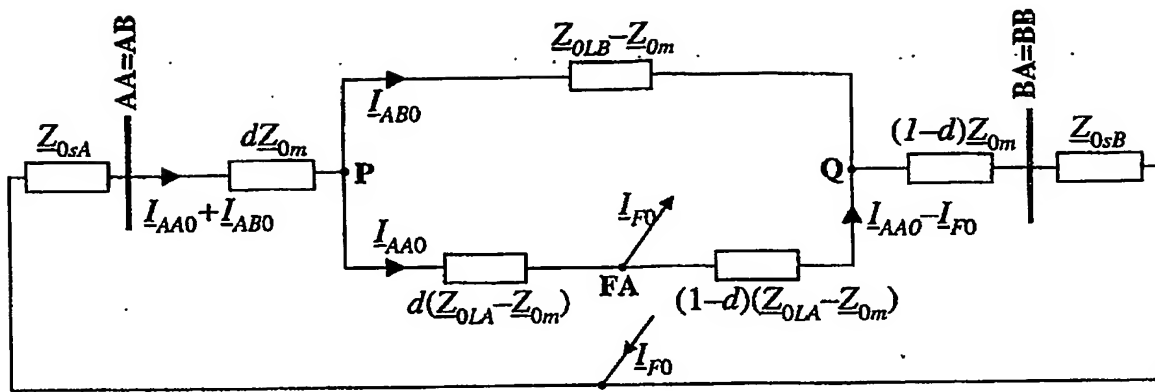


Figure 21b

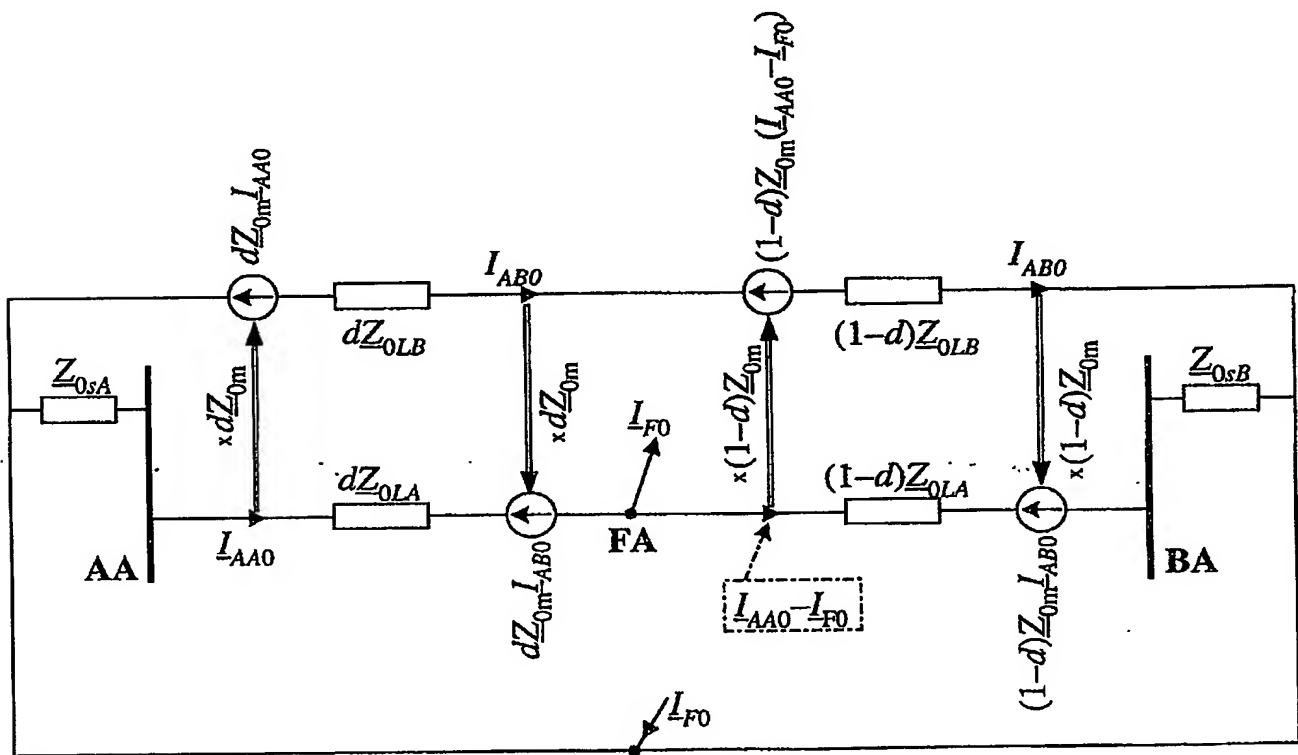


Figure 21c

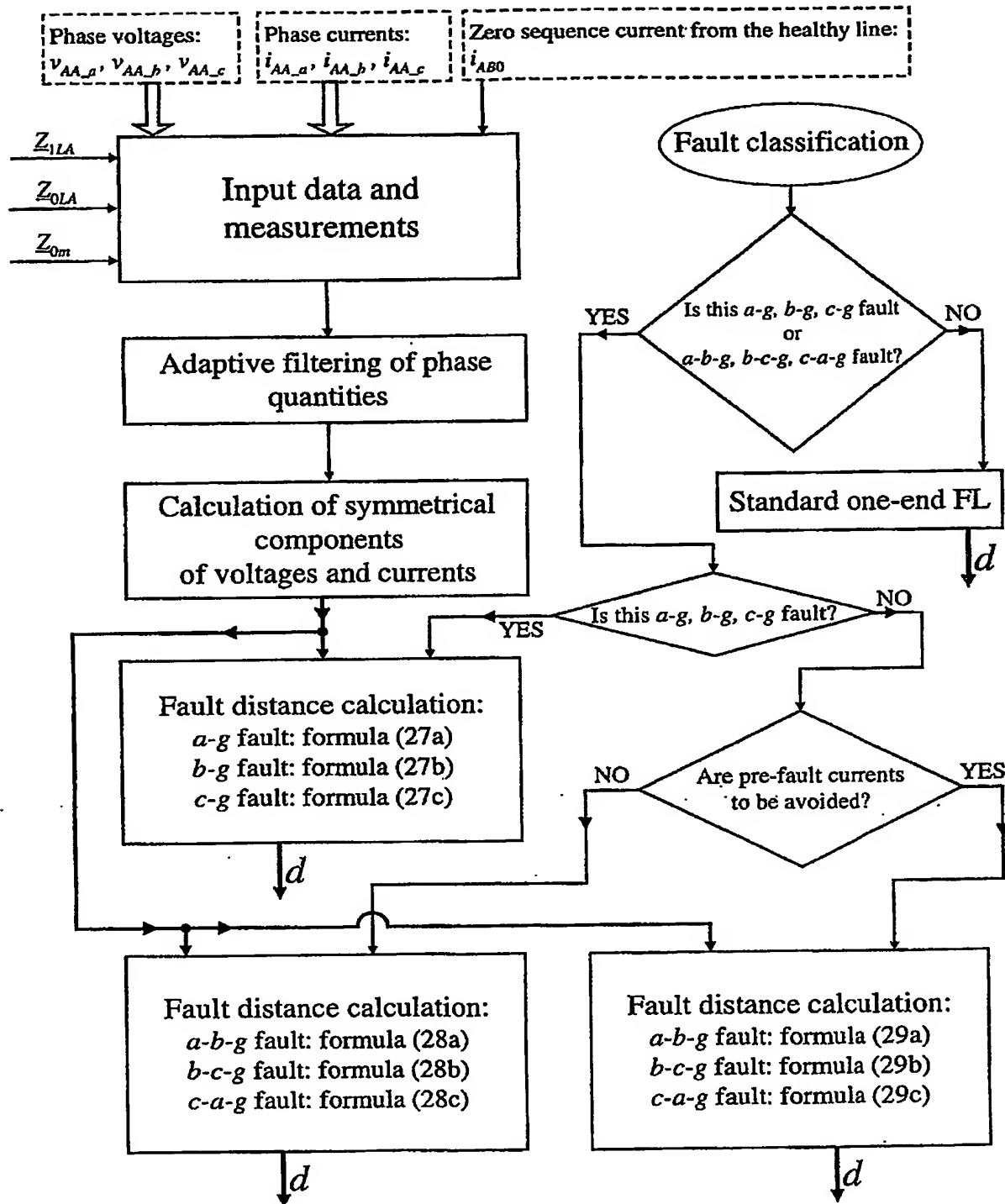


Figure 22

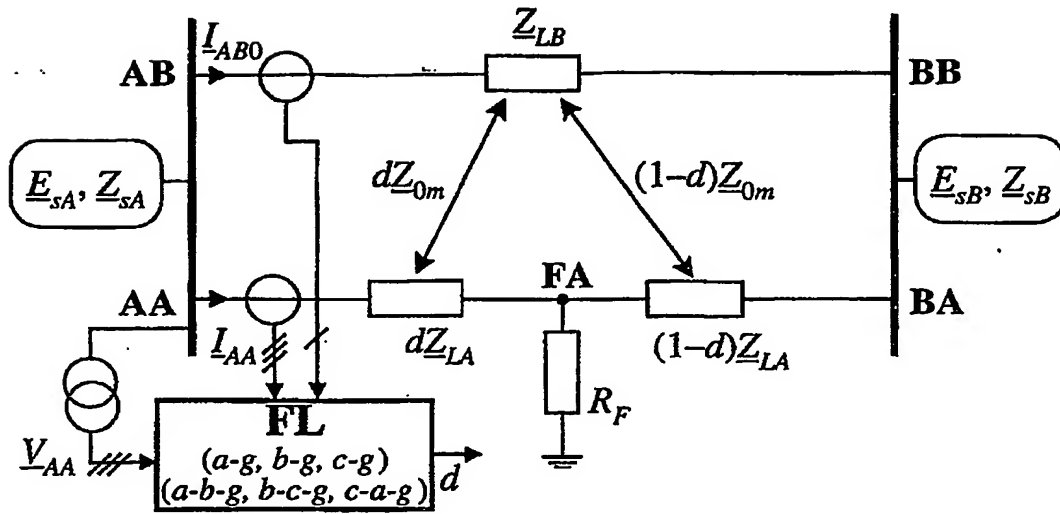


Figure 23

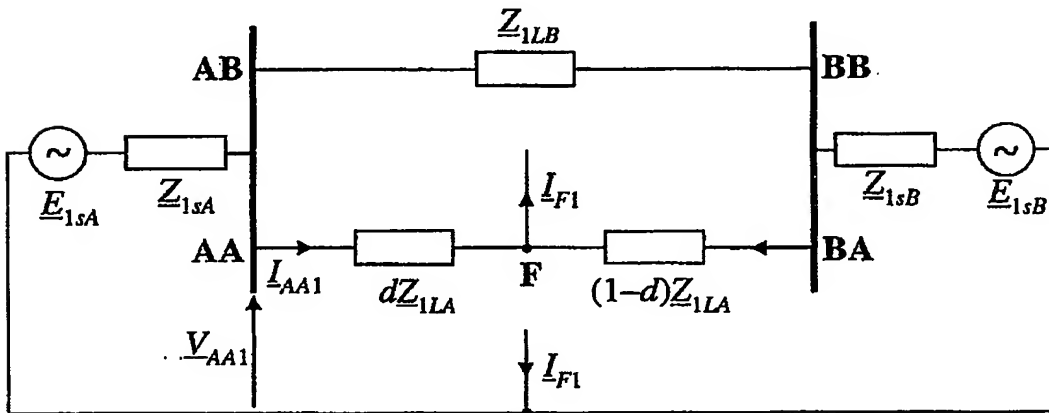


Figure 24a



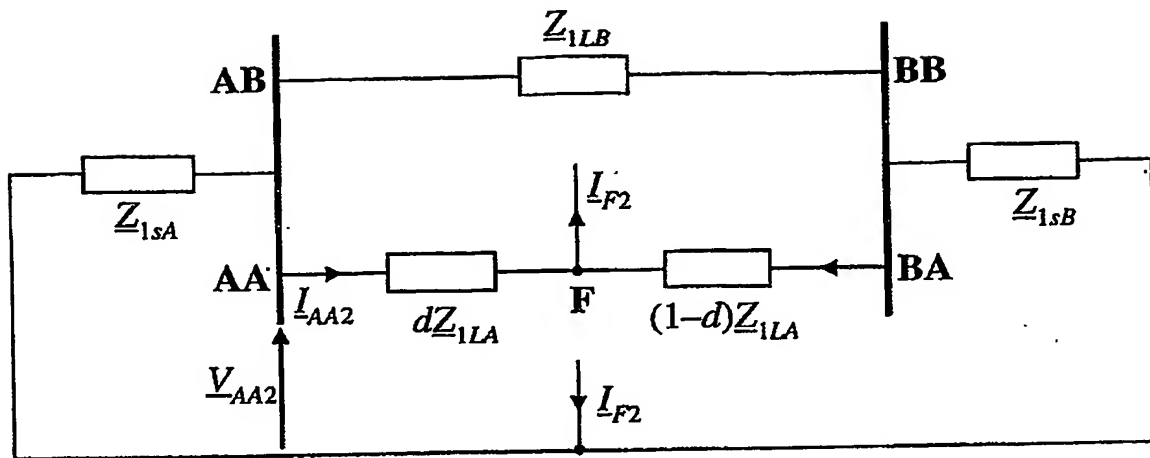


Figure 24b

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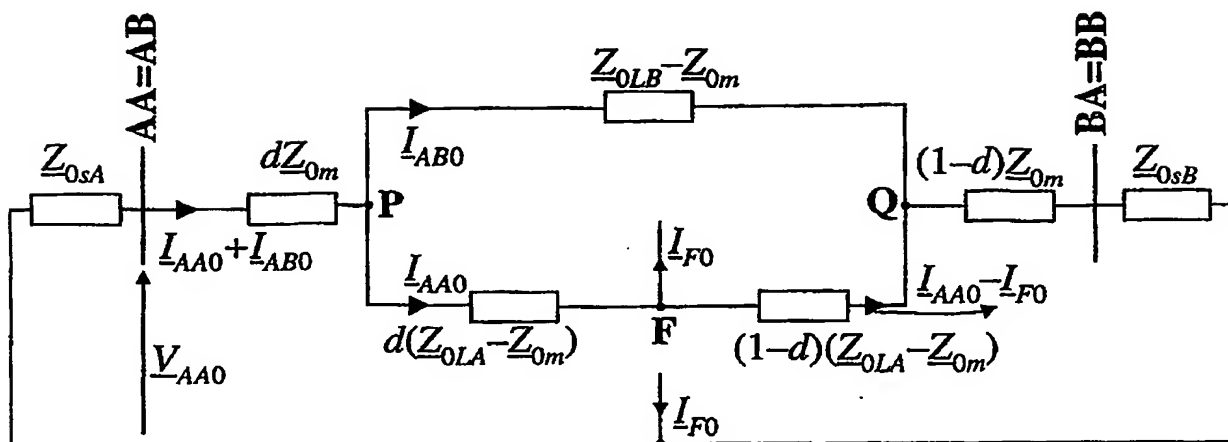


Figure 24c